A Five-Day Faculty Updation Programme (Offline)

on

PCB Design and Analysis

23rd – 27th Nov 2026

Organized Jointly by

Dr. A.P.J. Abdul Kalam Technical University Lucknow

and

Siemens EDA (Electronic Design Automation)

SIEMENS

Venue

Institute of Engineering & Technology

अभियांत्रिकी एवं प्रौद्योगिकी संस्थान

(An Autonomous Constituent Institute of Dr. A.P.J. Abdul Kalam Technical University Uttar Pradesh, Lucknow)

About the AKTU & IET Lucknow

Dr. A.P.J. Abdul Kalam Technical University (AKTU), Lucknow, is a premier state technical university established by the Government of Uttar Pradesh to promote technical education, research, and innovation. Institute of Engineering and Technology (IET), Lucknow, popularly known as the "Engineering College" of Lucknow, is one of the premier institutions under AKTU. Established in 1984 by the Government of Uttar Pradesh, IET is an autonomous, AICTE-recognised constituent institute of AKTU. Formerly affiliated with the University of Lucknow (1984–1999), the Institute is committed to academic excellence, quality technical education, research, and technological advancement.

About Siemens EDA

Siemens Electronic Design Automation (EDA), a part of Siemens Digital Industries Software, is a global technology leader providing advanced software, hardware, and solutions for electronic design automation. Its comprehensive portfolio supports the design, simulation, verification, and manufacturing of integrated circuits (ICs), printed circuit boards (PCBs), and electronic systems. Siemens EDA focuses on innovation, digitalization, and AI-driven technologies to empower engineers, researchers, and industries in developing next-generation electronic products.

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Programme Coordinators

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Dr. Anurag Yadav, ECED
Dr. Shahneela Jamal Kidwai, ECED
Mr. Sandeep Kumar, Incubation Manager

About the FUP

The Faculty Updation Programme (FUP) on *PCB Design and Analysis*, jointly organized by **Dr. A.P.J. Abdul Kalam Technical University (AKTU), Lucknow, and the Institute of Engineering & Technology (IET), Lucknow**, in collaboration with **Siemens EDA**, is scheduled to be held from **23rd to 27th November, 2026** at IET, Lucknow.

The programme is designed to provide faculty members with comprehensive knowledge and practical exposure to advanced PCB design, simulation, and analysis using Siemens EDA tools. Participants will gain insights into PCB design methodologies, industry best practices, and how design platforms such as Xpedition can help streamline and optimize the PCB design flow.

The FUP will also introduce circuit simulation concepts and demonstrate how simulation can be used to evaluate circuit behaviour and validate designs before hardware implementation.

Furthermore, FUP will cover the fundamental principles of Signal Integrity (SI) and Power Integrity (PI) for PCB designs. Participants will learn about common SI/PI challenges and how simulation and analysis tools can be leveraged to identify potential issues, optimize designs, and achieve reliable system performance.

Resource Person

Mr. Pavan Kumar Nanduri
Siemens Industry Software (India) Pvt. Ltd.

Mr. Rakshith J.
Siemens Industry Software (India) Pvt. Ltd.

Important Dates

Last date for Online Registration: 14-11-2026 (50 Seats)

Confirmation by E-Mail: 19-11-2026.

FUP Duration: 23-11-2026 to 27-11-2026

Time: 09:00 AM – 05:30 PM

Note: University will provide tea, snacks, accommodation and meals during the FUP.

Registration

Registration Link:

https://forms.gle/TH8Z5G4RiRJ6gnjb7

Registration Fee- NIL (Free of Cost)

Scan the QR code below to register in the FUP

FUP Registration QR– PCB Design and Analysis

Certification

Certificates will be awarded to participants upon successful completion of the FUP with **100% attendance** (and submission of daily feedback).

Contact Details

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