



INSTITUTE OF ENGINEERING AND TECHNOLOGY
SITAPUR ROAD, LUCKNOW - 226021

Ref: IET/04/EB/2019-121

Date: 10.10.2019
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Advertisement for the post of Junior Research Assistant for the CST, UP sponsored project

Applications are invited for one post of Junior Research Assistant to work in a research project entitled "Development of Prototype Model for Instrument to detect Murmurs by simultaneously using ECG and PCG Signals" under the terms and conditions of UPCST. The project duration is of one year. The selected candidate will be paid a monthly fellowship of Rs 20000/- per month for JRA.

ELIGIBILITY CRITERIA

1. Educational Qualifications:

Essential: First class B.Tech /B.E. degree or M.Tech / M.E. in Electrical Engg / Electronics Engg / Electronics & Instrumentation Engg / Electrical & Electronics Engg / Instrumentation & Control Engg / Bio- Instrumentation/Computer Science or equivalent from a recognised university or institute.


Desirable: preference will be given to the candidates having experience in

- Exposure to Biomedical Instrumentation / Instrumentation / Measurement, and
- Programming skills with C/ MATLAB
- Digital Signal Processing, signal filtering (hardware)

2. **Age Limit:** 28 years as on 1st April 2018 for JRA

3. **Nationality:** Indian (only Indian Citizens who are *Resident of Uttar Pradesh* are eligible)

Candidates possessing the requisite qualifications are required to submit their applications on plain paper giving their Bio data by registered post or via email within 15 days from the date of advertisement to: **Dr Satyendra Singh, Associate Professor & Co-Investigator, Department of Electrical Engineering, Institute of Engineering and Technology, Sitapur Road, Lucknow, UP -226021.** The shortlisted candidates will be required to attend, with their original documents, an interview for which no TA DA will be paid.


(Prof. Bharti Dwivdi)
Principal Investigator

Dr. Satyendra Singh, Co- Investigator
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