

INSTITUTE OF ENGINEERING AND TECHNOLOGY LUCKNOW

(An Autonomous Constituent Institute of Dr. A.P.J. Abdul Kalam Technical University, Lucknow)



Evaluation Scheme & Syllabus

For

B. Tech. Third Year

(Electronics and Communication Engineering)

AS PER

AICTE MODEL CURRICULUM

[Effective from the Session: 2020-21]

ELECTRONICS AND COMMUNICATION ENGINEERING

B.Tech. V Semester

Electronics and Communication Engineering

| S. No. | Course Code | Course Title | Periods | | | Evaluation Scheme | | | | End Semester | | Total | Credits |
|--------|---------------|--|---------|---|---|-------------------|----|-------|----|--------------|----|------------|-----------|
| | | | L | T | P | CT | TA | Total | PS | TE | PE | | |
| 1 | KEC-501 | Integrated Circuits | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 2 | KEC-502 | Microprocessor & Microcontroller | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 3 | KEC-503 | Digital Signal Processing | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 4 | KEC-051-054 | Department Elective-I | 3 | 0 | 0 | 30 | 20 | 50 | | 100 | | 150 | 3 |
| 5 | KEC-055-058 | Department Elective-II | 3 | 0 | 0 | 30 | 20 | 50 | | 100 | | 150 | 3 |
| 6 | KEC-551 | Integrated Circuits Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 7 | KEC-552 | Microprocessor & Microcontroller Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 8 | KEC-553 | Digital Signal Processing Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 9 | KEC-554 | Mini Project/Internship ** | 0 | 0 | 2 | | | | 50 | | | 50 | 1 |
| 10 | KNC501/KNC502 | Constitution of India, Law and Engineering / Indian Tradition, Culture and Society | 2 | 0 | 0 | 15 | 10 | 25 | | 50 | | | NC |
| 11 | | MOOCs (Essential for Hons. Degree) | | | | | | | | | | | |
| | | Total | | | | | | | | | | 950 | 22 |

**The Mini Project or Internship (4weeks) conducted during summer break after IV Semester and will be assessed during Vth Semester.

Course Code

Course Title

Department Elective-I

| | |
|---------|--|
| KEC-051 | Computer Architecture and Organization |
| KEC-052 | Industrial Electronics |
| KEC-053 | VLSI Technology |
| KEC-054 | Advance Digital Design using Verilog |

Department Elective-II

| | |
|---------|---|
| KEC-055 | Electronics Switching |
| KEC-056 | Advance Semiconductor Device |
| KEC-057 | Electronics Measurement & Instrumentation |
| KEC-058 | Optical Communication |

ELECTRONICS AND COMMUNICATION ENGINEERING

B.Tech. VI Semester Electronics and Communication Engineering

| S. No. | Course Code | Course Title | Periods | | | Evaluation Scheme | | | | End Semester | | Total | Credits |
|--------|-------------------|--|---------|---|---|-------------------|----|-------|----|--------------|----|------------|-----------|
| | | | L | T | P | CT | TA | Total | PS | TE | PE | | |
| 1 | KEC-601 | Digital Communication | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 2 | KEC-602 | Control System | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 3 | KEC-603 | Antenna and Wave Propagation | 3 | 1 | 0 | 30 | 20 | 50 | | 100 | | 150 | 4 |
| 4 | | Department Elective–III | 3 | 0 | 0 | 30 | 20 | 50 | | 100 | | 150 | 3 |
| 5 | | Open Elective-I | 3 | 0 | 0 | 30 | 20 | 50 | | 100 | | 150 | 3 |
| 6 | KEC-651 | Digital Communication Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 7 | KEC-652 | Control System Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 8 | KEC-653 | Elective Lab | 0 | 0 | 2 | | | | 25 | | 25 | 50 | 1 |
| 9 | KNC601/ KNC602 | Constitution of India, Law and Engineering / Indian Tradition, Culture and Society | 2 | 0 | 0 | 15 | 10 | 25 | | 50 | | | NC |
| 10 | | MOOCs (Essential for Hons. Degree) | | | | | | | | | | | |
| | | Total | | | | | | | | | | 900 | 21 |

Course Code

Course Title

Department Elective-III

| | |
|---------|--|
| KEC-061 | Microcontroller & Embedded System Design |
| KEC-062 | Satellite Communication |
| KEC-063 | Data Communication Networks |
| KEC-064 | Analog Signal Processing |
| KEC-065 | Random Variables & Stochastic Process |

Course Code

Elective Lab

| | |
|----------|--|
| KEC-653A | Measurement & Instrumentation Lab |
| KEC-653B | Cad for Electronics Lab |
| KEC-653C | Microcontroller & Embedded System Design |

**B.Tech 3rd Year
V Semester
Syllabus**

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|----------------------------|-----------------|------------------|
| KEC-501 | INTEGRATED CIRCUITS | 3L:1T:0P | 4 Credits |
|----------------|----------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|------|--|------------|
| I | The 741 IC Op-Amp: General operational amplifier stages (bias circuit, the input stage, the second stage, the output stage, short circuit protection circuitry), device parameters, DC and AC analysis of input stage, second stage and output stage, gain, frequency response of 741, a simplified model, slew rate, relationship between ft and slew rate. | 8 |
| II | Linear Applications of IC Op-Amps: Op-Amp based V-I and I-V converters, instrumentation amplifier, generalized impedance converter, simulation of inductors. Active Analog filters: Sallen Key second order filter, Designing of second order low pass and high pass Butterworth filter, Introduction to band pass and band stop filter, all pass active filters, KHN Filters. Introduction to design of higher order filters. | 8 |
| III | Frequency Compensation & Nonlinearity: Frequency Compensation, Compensation of two stage Op-Amps, Slewing in two stage Op-Amp. Nonlinearity of Differential Circuits, Effect of Negative feedback on Nonlinearity. Non-Linear Applications of IC Op-Amps: Basic Log–Anti Log amplifiers using diode and BJT, temperature compensated Log-Anti Log amplifiers using diode, peak detectors, sample and hold circuits. Op-amp as a comparator and zero crossing detector, astable multivibrator & monostable multivibrator. Generation of triangular waveforms, analog multipliers and their applications. | 4 8 |
| IV | Digital Integrated Circuit Design: An overview, CMOS logic gate circuits basic structure, CMOS realization of inverters, AND, OR, NAND and NOR gates. Latches and Flip flops: the latch, CMOS implementation of SR flip-flops, a simpler CMOS implementation of the clocked SR flip-flop, CMOS implementation of J-K flip-flops, D flip- flop circuits. | 6 |
| V | Integrated Circuit Timer: Timer IC 555 pin and functional block diagram, Monostable and Astable multivibrator using the 555 IC. Voltage Controlled Oscillator: VCO IC 566 pin and functional block diagram and applications. Phase Locked Loop (PLL): Basic principle of PLL, block diagram, working, Ex-OR gates and multipliers as phase detectors, applications of PLL. | 6 |

Text Book:

1. Microelectronic Circuits, Sedra and Smith, 7th Edition, Oxford, 2017.
2. Behzad Razavi: Design of Analog CMOS Integrated Circuits, TMH

Reference Books:

1. Gayakwad: Op-Amps and Linear Integrated Circuits, 4th Edition Prentice Hall of India, 2002.
2. Franco, Analog Circuit Design: Discrete & Integrated, TMH, 1st Edition.
3. Salivahnan, Electronics Devices and Circuits, TMH, 3rd Edition, 2015
4. Millman and Halkias: Integrated Electronics, TMH, 2nd Edition, 2010

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Explain complete internal analysis of Op-Amp 741-IC.
2. Examine and design Op-Amp based circuits and basic components of ICs such as various types of filter.
3. Implement the concept of Op-Amp to design Op-Amp based non-linear applications and wave-shaping circuits.
4. Analyse and design basic digital IC circuits using CMOS technology.
5. Describe the functioning of application specific ICs such as 555 timer ,VCO IC 566 and PLL.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|---|-----------------|------------------|
| KEC-502 | MICROPROCESSOR & MICROCONTROLLER | 3L:1T:0P | 4 Credits |
|----------------|---|-----------------|------------------|

| Unit | Topics | Lectures |
|------|--|----------|
| I | Introduction to Microprocessor: Microprocessor architecture and its operations, Memory, Input & output devices, The 8085 MPU- architecture, Pins and signals, Timing Diagrams, Logic devices for interfacing, Memory interfacing, Interfacing output displays, Interfacing input devices, Memory mapped I/O. | 8 |
| II | Basic Programming concepts: , Flow chart symbols, Data Transfer operations, Arithmetic operations, Logic Operations, Branch operation, Writing assembly language programs, Programming techniques: looping, counting and indexing. Additional data transfer and 16 bit arithmetic instruction, Logic operation: rotate, compare, counter and time delays, 8085 Interrupts. | 8 |
| III | 16-bit Microprocessors (8086): Architecture, Pin Description, Physical address, segmentation, memory organization, Addressing modes. Peripheral Devices: 8237 DMA Controller, 8255 programmable peripheral interface, 8253/8254 programmable timer/counter, 8259 programmable interrupt controller, 8251 USART and RS232C. | 8 |
| IV | 8051 Microcontroller Basics: Inside the Computer, Microcontrollers and Embedded Processors, Block Diagram of 8051, PSW and Flag Bits, 8051 Register Banks and Stack, Internal Memory Organization of 8051, IO Port Usage in 8051, Types of Special Function Registers and their uses in 8051, Pins Of 8051. Memory Address Decoding, 8031/51 Interfacing With External ROM And RAM. 8051 Addressing Modes. | 8 |
| V | Assembly programming and instruction of 8051: Introduction to 8051 assembly programming, Assembling and running an 8051 program, Data types and Assembler directives, Arithmetic, logic instructions and programs, Jump, loop and call instructions, IO port programming. Programming 8051 Timers. Serial Port Programming, Interrupts Programming, Interfacing: LCD & Keyboard Interfacing, ADC, DAC & Sensor Interfacing, External Memory Interface, Stepper Motor and Waveform generation. | 8 |

Text Books:

1. Ramesh Gaonkar, "Microprocessor Architecture, Programming, and Applications with the 8085", 6th Edition, Penram International Publication (India) Pvt. Ltd., 2013
2. D. V. Hall : Microprocessors Interfacing, TMH 3rd Edition,
3. Mazidi Ali Muhammad, Mazidi Gillispie Janice, and McKinlay Rolin D., "The 8051 Microcontroller and Embedded Systems using Assembly and C", Pearson, 2nd Edition, 2006

Reference Books:

1. Kenneth L. Short, "Microprocessors and programmed Logic", 2nd Ed, Pearson Education Inc., 2003
2. Barry B. Brey, "The Intel Microprocessors, 8086/8088, 80186/80188, 80286, 80386, 80486, Pentium, PentiumPro Processor, PentiumII, PentiumIII, Pentium IV, Architecture, Programming & Interfacing", Eighth Edition, Pearson Prentice Hall, 2009.
3. Shah Satish, "8051 Microcontrollers MCS 51 Family and its variants", Oxford, 2010

Course Outcomes: At the end of this course students will demonstrate the ability to

1. Demonstrate the basic architecture of 8085.
2. Illustrate the programming model of microprocessors & write program using 8085 microprocessor.
3. Demonstrate the basics of 8086 Microprocessor and interface different external Peripheral Devices like timer, USART etc. with Microprocessor (8085/8086).
4. Compare Microprocessors & Microcontrollers, and comprehend the architecture of 8051 microcontroller
5. Illustrate the programming model of 8051 and implement them to design projects on real time problems.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|----------------------------------|-----------------|------------------|
| KEC-503 | DIGITAL SIGNAL PROCESSING | 3L:1T:0P | 4 Credits |
|----------------|----------------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|------|--|----------|
| I | <p>Introduction to Digital Signal Processing: Basic elements of digital signal processing, advantages and disadvantages of digital signal processing, Technology used for DSP.</p> <p>Realization of Digital Systems: Introduction- basic building blocks to represent a digital system, recursive and non-recursive systems, basic structures of a digital system: Canonic and Non-Canonic structures. IIR Filter Realization: Direct form, cascade realization, parallel form realization, Ladder structures- continued fraction expansion of $H(z)$, example of continued fraction, realization of a ladder structure, design examples. FIR Filter Realization: Direct, Cascade, FIR Linear Phase Realization and design examples.</p> | 8 |
| II | <p>Infinite Impulse Response Digital (IIR) Filter Design: Introduction to Filters, Impulse Invariant Transformation, Bi-Linear Transformation, All- Pole Analog Filters: Butterworth and Chebyshev, Design of Digital Butterworth and Chebyshev Filters, Frequency Transformations.</p> | 8 |
| III | <p>Finite Impulse Response Filter (FIR) Design: Windowing and the Rectangular Window, Gibb's phenomenon, Other Commonly Used Windows (Hamming, Hanning, Bartlett, Blackmann, Kaiser), Examples of Filter Designs Using Windows. Finite Word length effects in digital filters: Coefficient quantization error, Quantization noise – truncation and rounding, Limit cycle oscillations-dead band effects.</p> | 8 |
| IV | <p>DFT & FFT: Definitions, Properties of the DFT, Circular Convolution, Linear Convolution using Circular Convolution, Decimation in Time (DIT) Algorithm, Decimation in Frequency (DIF) Algorithm.</p> | 8 |
| V | <p>Multirate Digital Signal Processing (MDSP): Introduction, Decimation, Interpolation, Sampling rate conversion: Single and Multistage, applications of MDSP- Subband Coding of Speech signals, Quadrature mirror filters, Advantages of MDSP.</p> | 8 |

Text Books:

1. John G Prokias, Dimitris G Manolakis, Digital Signal Processing. Pearson , 4th Edition, 2007
2. Johnny R. Johnson, Digital Signal Processing, PHI Learning Pvt Ltd., 2009.
3. S. Salivahanan, A. Vallavaraj, Digital Signal Processing, TMH, 4th Edition 2017.
4. Oppenheim & Schaffer, Digital Signal Processing. Pearson Education 2015
5. S.K. Mitra, 'Digital Signal Processing–A Computer Based Approach, TMH, 4th Edition.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Design and describe different types of realizations of digital systems (IIR and FIR) and their utilities.
2. Select design parameters of analog IIR digital filters (Butterworth and Chebyshev filters) and implement various methods such as impulse invariant transformation and bilinear transformation of conversion of analog to digital filters.
3. Design FIR filter using various types of window functions.
4. Define the principle of discrete Fourier transform & its various properties and concept of circular and linear convolution. Also, students will be able to define and implement FFT i.e. a fast computation method of DFT.
5. Define the concept of decimation and interpolation. Also, they will be able to implement it in various practical applications.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|---|-----------------|------------------|
| KEC-051 | Computer Architecture and Organization | 3L:0T:0P | 3 Credits |
|----------------|---|-----------------|------------------|

| Unit | Topics | Lectures |
|-------------|---|-----------------|
| I | Introduction to Design Methodology: System Design – System representation, Design Process, the gate level (revision), the register level components and PLD (revision), register level design The Processor Level: Processor level components, Processor level design. | 8 |
| II | Processor basics: CPU organization- Fundamentals, Additional features Data Representation - Basic formats, Fixed point numbers, Floating point numbers. Instruction sets - Formats, Types, Programming considerations. | 8 |
| III | Data path Design: Fixed point arithmetic - Addition and subtraction, Multiplication and Division, Floating point arithmetic, pipelining. | 8 |
| IV | Control Design: basic concepts - introduction, hardwired control, Micro programmed control -introduction, multiplier control unit, CPU control unit, Pipeline control- instruction pipelines, pipeline performance. | 8 |
| V | Memory organization: Multi level memories, Address translation, Memory allocation, Caches - Main features, Address mapping, structure vs performance, System Organization: Communication methods- basic concepts, bus control. Introduction to VHDL. | 8 |

Text Book:

1. John P Hayes "Computer Architecture and Organization", 3rd Edition McGraw Hill Publication. (2017)
2. M Morris Mano, "Computer System Architecture", 3rd Edition ,Pearson,. (2017)

Reference Books:

1. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization and Embedded Systems", McGraw Hill Publication. (2009)
2. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software Interface", Elsevier Publication. (2007)

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Discuss about the basic concepts of system design methodology and processor level design.
2. Explain the basics of processor and basic formats of data representation.
3. Perform fixed and floating point arithmetic operations.
4. Describe the basic concepts of control design and pipeline performance.
5. Explain the architecture and functionality of central processing unit.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|-------------------------------|-----------------|------------------|
| KEC-052 | INDUSTRIAL ELECTRONICS | 3L:0T:0P | 3 Credits |
|----------------|-------------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|-------------|---|-----------------|
| I | Introduction to Power Switching Devices: Description of working & constructional features, Switching Characteristics, ratings and Applications of Power Transistor, Power MOSFET, SCR, DIAC, TRIAC, IGBT and MCT. | 8 |
| II | SCR Performance and Applications: Protection of SCR, SCR Triggering and Commutation Circuits/Methods, Series and Parallel operation of SCR, two transistor model of SCR, , Describe Construction & Working of Opto- Isolators, Opto-TRIAC, Opto-SCR. | 8 |
| III | Power Converter Performance & Applications: Introduction to Basic Power Converters Architecture - Single Phase, their performance under different types of Loads, Average/RMS output Voltage & Current, Freewheeling Diode, Feedback Diode, State Relay using Opto SCR, SMPS and UPS functioning through Block Diagrams. | 8 |
| IV | Timers & Delay Elements, High Frequency Power Heating, Sensor and Actuators: RC Base Constant Timers, Timer Circuits using SCR, IC-555, Programmable Timer and their Industrial Applications, Induction Heating and Dielectric Heating System and Their Applications, Sensors, Transducers, and Transmitters for Measurement, Control & Monitoring : Thermoresistive Transducer, Photoconductive Transducers, Pressure Transducers, Flow Transducers, Level Sensors, Speed Sensing, Vibration Transducers, Variable-Frequency Drives, Stepper Motors and Servomotor Drives. | 8 |
| V | Automation and Control: Data Communications for Industrial Electronics, Telemetry, SCADA & Automation, AC & DC Drives, Voltage & Power Factor Control through Solid State Devices, Soft Switching, Industrial Robots. | 8 |

Text Books:

1. M.H. Rashid, "Power Electronics: Circuits, Devices & Applications", Pearson, 4rd Edition, 2013.
2. P.C.Sen, "Power Electronics", McGraw Hill Education (India) Pvt. Ltd 2nd Ed, 2017
3. V.R. Moorthy, "Power Electronics: Devices, Circuits and Industrial Applications" Oxford University Press, 2007.
4. B. Paul, Industrial Electronic and Control, Prentice Hall of India Private Limited (2004).
5. Ned Mohan, T.M. Undeland and W.P. Robbins, "Power Electronics: Converters, Applications and Design", Wiley India Ltd, 2008.
6. P.S. Bhimbra, "Power Electronics", Khanna Publishers.

ELECTRONICS AND COMMUNICATION ENGINEERING

Reference Books:

1. Thomas E. Kissell, Industrial Electronics: Applications for Programmable Controllers, Instrumentation and Process Control, and Electrical Machines and Motor Controls, 3rd edition, 2003, Prentice Hall.
2. Chakrabarti & Rai, "Fundamentals of Power Electronics & Drives" Dhanpat Rai & Sons.
3. S.N.Singh, "A Text Book of Power Electronics" Dhanpat Rai & Sons.
4. G.K. Dubey, Power Semiconductor Controlled Drives, Prentice Hall inc. (1989).

Course Outcomes: At the end of this course students will be able to:

1. Describe the characteristics, operation of power switching devices and identify their ratings and applications.
2. Recognize the requirement of SCR Protection and describe the Functioning of SCR.
3. Analyze and design Power Converter based on SCR for various Industrial Applications.
4. Explain High Frequency Heating Systems, Timers, Relevant Sensors & Actuator and their application in industrial setting.
5. Explain and apply Data Communication, Telemetry & SCADA System in industrial applications.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|------------------------|-----------------|------------------|
| KEC-053 | VLSI TECHNOLOGY | 3L:0T:0P | 3 Credits |
|----------------|------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|-------------|--|-----------------|
| I | Introduction To IC Technology: SSI, MSI, LSI, VLSI Integrated Circuits. Crystal Growth and Wafer Preparation: Electronic Grade Silicon, Czochralski Crystal Growth, Silicon Shaping, Processing Considerations. Wafer Cleaning Technology - Basic Concepts, Wet cleaning, Dry cleaning | 8 |
| II | Epitaxy: Vapor-Phase Epitaxy, Molecular Beam Epitaxy, Silicon on Insulators, Epitaxial Evaluation. Oxidation: Growth Kinetics, Thin Oxides, Oxidation Techniques and Systems, Oxides Properties. | 8 |
| III | Lithography: Optical Lithography, Electron beam lithography, Photo masks, Wet Chemical Etching. Dielectric and Polysilicon Film Deposition: Deposition Processes of Polysilicon, Silicon Dioxide, Silicon Nitride. | 8 |
| IV | Diffusion: Models of diffusion in solids, Fick's 1-Dimensional diffusion equation, Diffusion of Impurities in Silicon and Silicon Dioxide, Diffusion Equations, Diffusion Profiles, Diffusion Furnace, Solid, Liquid and Gaseous Sources, Ion-Implantation: Ion-Implantation Technique, Range Theory, Implantation Equipment. | 8 |
| V | Metallization: Metallization Application, Metallization Choices, Physical Vapor Deposition, Vacuum Deposition, Sputtering Apparatus. Packaging of VLSI devices: Package Types, Packaging Design Consideration, VLSI Assembly Technologies, Package Fabrication Technologies, CMOS fabrication steps. | 8 |

Text Books:

1. S. M. Sze, "VLSI Technology", McGraw Hill Publication, 2nd Edition 2017
2. S.K. Ghandhi, "VLSI Fabrication Principles", Willy-India Pvt. Ltd, 2008

Reference Books:

1. J. D. Plummer, M. D. Deal and Peter B. Griffin, "Silicon VLSI Technology: Fundamentals, Practice and Modeling", Pearson Education Publication, 2009
2. Stephen A. Campbell, "Fabrication Engineering at the Micro and Nano scale", Oxford University Press, 2013

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Interpret the basics of crystal growth, wafer preparation and wafer cleaning.
2. Evaluate the process of Epitaxy and oxidation.
3. Differentiate the lithography, etching and deposition process.
4. Analyze the process of diffusion and ion implantation
5. Express the basic process involved in metallization and packaging.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|--|-----------------|------------------|
| KEC 054 | ADVANCED DIGITAL DESIGN USING VERILOG | 3L:0T:0P | 3 Credits |
|----------------|--|-----------------|------------------|

| Unit | Topic | Lectures |
|-------------|--|-----------------|
| I | Introduction to Mixed Logic, Logic Representation and Minimization with cost, Multiple output minimization, Entered Variable K- Map including don't care handling, XOR Pattern Handling. | 8 |
| II | Combinational Circuit Design, Multiplexers, Decoders, Encoders, Code Comparators, Adders, Subtractors, Multipliers, Introduction to Verilog, Behavioral and Structural specification of logic circuits, Boolean function implementation using Verilog, Timing Analysis, Hazard Detection and Elimination | 8 |
| III | Synchronous Sequential Circuits Design, Mapping Algorithm, Synchronous State Machines, ASM Charts, Asynchronous Sequential Circuit Design, Races, Multi-level minimization and optimization. | 8 |
| IV | Factoring, Decomposition, BDD, Ordered BDD, LPDD, Fault Detection and Analysis in combinational and sequential systems, Path Sensitization method, Boolean Difference Method, Initial State Method. | 8 |
| V | Study of programmable logic families, PLD, CPLD, FPGA, ASIC, PLA, Architectures, Design of Combinational and sequential circuits using CPLD and FPGA, Design Examples. | 8 |

Text Books:

1. Richard F. Tinker, "Engineering Digital Design", Academic Press.
2. Parag K. Lala, "Digital system Design Using PLDs", PHI India Ltd.
3. Stephen Brown and Zvonko Vranesiv, "Fundamental of Digital Logic with Verilog Design", Tata McGraw Hill.

Reference Books:

1. John Williams, "Digital VLSI Design with Verilog", Springer Publication..
2. Samuel C. Lee, "Digital Circuit and Logic Design", PHI India Ltd.
3. Alexander Miczo, "Digital Logic Testing and Simulation", Wiley Interscience.

COURSE OUTCOME: After completion of the course student will be able to

1. Describe mixed logic circuits and their implementation.
2. Implement combinational circuits using mixed logic and Verilog.
3. Design sequential circuits using mixed logic and Verilog with mapping of Algorithm.
4. Understand faults and its elimination in sequential and combinational circuits.
5. Understand the working of programmable logic families.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|-----------------------------|-----------------|------------------|
| KEC-055 | ELECTRONIC SWITCHING | 3L:0T:0P | 3 Credits |
|----------------|-----------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|-------------|---|-----------------|
| I | Evolution of switching systems: Introduction, Message switching, Circuits switching, Functions of a switching system, Register translator-senders, Distribution frames, Crossbar switch, A general trucking, Electronic switching, Reed- electronic system, Digital switching systems. | 8 |
| II | Digital Switching: Switching functions, Space Division Switching, Time Division Switching, Two-Dimensional Switching, Digital Cross-Connect Systems, Digital Switching in an Analog Environment. | 8 |
| III | Telecom Engineering: Network Traffic Load and Parameters, Grade of Service and Blocking Probability, Modeling Switching Systems, Incoming Traffic and Service Time Characterization, Blocking models and Loss Estimates, Delay Systems | 8 |
| IV | Control of switching systems: Introduction, Call-processing functions, Common control, Reliability, availability and security; Stored-program control. Signaling: Introduction, Customer line signaling, Audio-frequency junctions and trunk circuits, FDM carrier systems, PCM signaling, Inter-register signalling, Common-channel signaling principles, CCITT signaling system no. 6 and 7, Digital customer line signaling. | 8 |
| V | Packet Switching: Packet Switching, Statistical Multiplexing, Routing Control (dynamic routing, virtual circuit routing and fixed-path routing), Flow Control, X.25, Frame Relay, TCP/IP ATM Cells, ATM Service Categories, ATM Switching (ATM Memory Switch, Space-Memory Switch, Memory-Space Switch, Memory-Space Memory switch, Banyan Network Switch, Clos Networks). | 8 |

Text Book:

1. Thiagarajan Viswanathan & Manav Bhatnagar, "Telecommunication Switching Systems and Networks", PHI, 2018
2. J.E. Flood, "Telecommunication Switching, Traffic and Networks", Pearson Education 2016.
3. John C. Bellamy, "Digital Telephony", John Wiley, 3rd Ed, 2006

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Describe the fundamentals of circuit switching and distinguish complex telephone systems.
2. Differentiate the fundamentals of Space division switching and time division switching.
3. Design, develop and evaluate the telecom traffic to meet defined specifications and needs.
4. Identify the control of switching networks and signalling concepts.
5. Classify the engineering concepts of packet switching and routing which will help to design various switch architectures for future research work.

ELECTRONICS AND COMMUNICATION ENGINEERING

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|----------------|--------------------------------------|-----------------|------------------|
| KEC-056 | ADVANCE SEMICONDUCTOR DEVICES | 3L:0T:0P | 3 Credits |
|----------------|--------------------------------------|-----------------|------------------|

| Unit | Topics | Lectures |
|------|--|----------|
| I | Physics and Properties of Semiconductors: Introduction, Crystal Structure, Energy Bands and Energy Gap, Carrier Concentration at Thermal Equilibrium, Carrier-Transport Phenomena. Phonon, Optical, and Thermal Properties, Heterojunctions and Nanostructures, Basic Equations and Examples. <i>p-n</i> Junctions, Introduction, Depletion Region, Current-Voltage Characteristics, Junction Breakdown, Transient Behavior and Noise, Terminal Functions, Heterojunctions. Metal-Semiconductor Contacts, Metal-Insulator - Semiconductor Capacitors. | 8 |
| II | Bipolar Transistors: Static Characteristics, Microwave Characteristics, Related Device Structures, Heterojunction Bipolar Transistor. MOSFETs: Basic Device Characteristics, Nonuniform Doping and Buried-Channel Device, Device Scaling and Short-Channel Effects, MOSFET Structures, Circuit Applications, Nonvolatile Memory Devices, Single-Electron Transistor. JFETs, MESFETs, and MODFETs | 8 |
| III | Tunnel Devices: Tunnel Diode, Related Tunnel Devices, Resonant-Tunneling Diode. IMPATT Diodes: Static Characteristics, Dynamic Characteristics, Power and Efficiency, Noise Behavior, Device Design and Performance, BARITT Diode, TUNNETT Diode. | 8 |
| IV | Transferred-Electron and Real-Space-Transfer Devices Thyristors and Power Devices Photonic Devices and Sensors: Radioactive Transitions, Light-Emitting Diode (LED), Laser Physics, Laser Operating Characteristics, Specialty Lasers. | 8 |
| V | Photodetectors and Solar Cells: Photoconductor, Photodiodes, Avalanche Photodiode, Phototransistor, Charge-Coupled Device (CCD), Metal-semiconductor-Metal Photodetector, Quantum-Well Infrared Photodetector, Solar Cell. Sensors: Thermal Sensors, Mechanical Sensors, Magnetic Sensors, Chemical Sensors. | 8 |

Text Book:

1. S. M. Sze, Kwok K. NG, "Physics of Semiconductor Devices", 3rd edition, Wiley Publication, 2015
2. Jacob Millman, Christos C. Halkias, Satyabrata Jit, Electronic Devices and Circuits. Publisher: TMH, 4th edition 2015.
3. Ben G. Streetman & S K Banerjee, Solid State Electronic Devices, Pearson 7th Edition, 2015
4. Pierret, Robert F., Semiconductor device fundamentals. 2nd Edition, Pearson Education India, 2015.

Course Outcomes: At the end of this course students will able to

1. Explain the behavior of BJT and MOSFET in DC biasing and as CE amplifier circuit.
2. Describe the Tunnel diode and IMPATT diode.
3. Explain the basics of Light-Emitting Diode (LED) and evaluate the performance of Photoconductor and photodiode.
4. Distinguish the performance of Photoconductor, photodiode, Phototransistor, Charge-Coupled Device
5. Analyze the functioning of Metal-Semiconductor-Metal Photodetector.

ELECTRONICS AND COMMUNICATION ENGINEERING

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| KEC-057 | ELECTRONIC MEASUREMENTS & INSTRUMENTATION | 3L:0T:0P | 3 CREDITS |
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| Unit | Topics | Lectures |
|------|--|----------|
| I | Electrical Measurements: Measurement system, Characteristics of instruments, Methods of measurement, Errors in Measurement & Measurement standards, Measurement error combination. Review of indicating and integrating instruments: PMMC instrument, Galvanometer, DC ammeter, DC voltmeter, Series ohm meter. | 8 |
| II | Electronic Instruments: Transistor voltmeter circuits, AC electronic voltmeter, current measurement with electronic instruments, probes. Digital voltmeter systems: Digital multimeter, digital frequency meter Instrument calibration: Comparison method, digital multimeter as standard instrument, Calibration instrument. | 8 |
| III | Measuring Methods: Voltmeter and Ammeter methods, Wheatstone bridge, Measurement of low, medium and high resistances, Insulation resistance measurement, AC bridges for measurement of inductance and capacitance. | 8 |
| IV | Electronic Measurements: Electronic instruments: Wattmeter & Energy meter. Time, Frequency and phase angle measurements using CRO; Storage oscilloscope, Spectrum & Wave analyzer, Digital counter & Frequency meter, Q meter | 8 |
| V | Instrumentation: Transducers, classification & selection of transducers, strain gauges, Thermistors, Thermocouples, LVDT, Inductive & capacitive transducers, Piezoelectric and Hall-effect transducers, Measurement of motion, force, pressure, temperature, flow and liquid level. | 8 |

Text Book:

1. A K Sawhney, "Electrical & Electronic Measurement & Instrument", Dhanpat Rai & Sons, India (2015).
2. BC Nakra & K. Chaudhary, "Instrumentation, Measurement and Analysis," TMH, 2nd Edition (2009).
3. WD Cooper, "Electronic Instrument & Measurement Technique", Prentice Hall International (2001).
4. E. O. Doebelin, "Measurements systems: Applications and Design", 6th Edition, Tata McGraw Hil 2017.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Classify the Instrumentation and Measurement system and various measurement errors.
2. Analyze and design voltmeter circuits, AC electronic voltmeter, digital frequency meter and current measurement with electronic instruments.
3. Evaluate various resistance and impedance measuring methods using Bridges and Q-meter.
4. Analyze fundamental operation of CRO and some special type of oscilloscopes like DSO, Sampling oscilloscope.
5. Demonstrate calibration method to calibrate various instruments and classify transducers like for force, pressure, motion, temperature measurement etc.

ELECTRONICS AND COMMUNICATION ENGINEERING

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| KEC-058 | OPTICAL COMMUNICATION | 3L:0T:0P | 3 Credits |
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| Unit | Topics | Lectures |
|------|---|----------|
| I | <p>Introduction to Optical Communication: Optical Spectral Band with Operating Windows, General Communication System, Optical Communication System with its advantages.</p> <p>Optical Fiber Waveguides: Ray Theory of Transmission with TIR, Acceptance Angle, Numerical Aperture and Skew Rays, Electromagnetic Mode Theory for Optical Propagation, Modes in a Planar Guide, Phase and Group Velocity, Phase Shift with Total Internal Reflection, Evanescent Field, Goos-Haenchen Shift, Cylindrical Fiber Modes, Mode Coupling, Step Index fibers Vs Graded Index fibers, Single Mode Fibers- Cut off wavelength, MFD & Spot Size.</p> | 08 |
| II | <p>Signal Loss in Optical Fibers: Attenuation, Material Absorption Losses (Intrinsic and Extrinsic absorption), types of Linear and Non-Linear Scattering Losses, Fiber Bending Losses, Kerr Effect.</p> <p>Dispersion: Introduction with its types: Chromatic / Intramodal Dispersion (Material and Waveguide Dispersion), Intermodal dispersion (for MSI and MGI fibers), Overall (Total) Fiber Dispersion in Multimode and Single Mode Fiber, Dispersion Modified Single Mode Fibers, Polarization & Fiber Birefringence.</p> | 08 |
| III | <p>Optical Sources: LEDs-Introduction to LEDs & Materials used for fabrication, LED Power and Efficiency, LED Structures, LED Characteristics, Modulation Bandwidth.</p> <p>Laser Diodes-Introduction, Optical Feedback & Laser Oscillations, Resonant Frequencies, Laser Modes, and Threshold Condition for Laser Oscillation, Laser Diode Rate Equations, Semiconductor injection Laser- Efficiency, Laser Single Mode operation, Reliability of LED & ILD.</p> | 08 |
| IV | <p>Power Launching in Fiber: Source to Fiber Power Launching and Coupling Techniques, Power Launching Vs Wavelength, Equilibrium Numerical Aperture.</p> <p>Photo Detectors: Introduction, Physical Principles of Photodiodes: The PIN Photo Detector, Avalanche Photodiodes, Temperature Effect on Avalanche Gain, Detector Response Time, Photo Detector Noise: Noise Sources, Signal to Noise Ratio, Comparison of Photo Detectors, Fundamental Receiver Operation with Digital Signal Transmission.</p> | 08 |
| V | <p>Digital Receiver Performance: Probability of Error / BER, Receiver Sensitivity & The Quantum Limit, Error Control Techniques, Eye Diagram Pattern Features, Coherent Detection: Homodyne Detection and Heterodyne Detection, Digital links: Point to Point Links, Power Penalties, Multichannel & Multiplexing Transmission Techniques, basic concept of Free Space Optics (FSO) based Communication System.</p> | 08 |

Text Book:

1. John M. Senior, "Optical Fiber Communications", Pearson, 3rd Edition, 2010.
2. Gerd Keiser, "Optical Fiber Communications", McGraw Hill, 5th Edition, 2013.
3. Govind P. Agrawal, "Fiber Optic Communication Systems", John Wiley, 3rd Edition, 2004.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Define and explain the basic concepts and theory of optical communication.
2. Describe the signal losses with their computation and dispersion mechanism occurring inside the optical fiber cable.
3. Differentiate the optical sources used in optical communication with their comparative study.
4. Identify different optical components on receiver side; assemble them to solve real world problems related to optical communication systems.
5. Evaluate the performance of an optical receiver to get idea about power budget and ultimately be an engineer with adequate knowledge in optical domain.

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| KEC-551 | INTEGRATED CIRCUITS LAB | 0L:0T:2P | 1 Credit |
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SUGGESTIVE LIST OF EXPERIMENTS:

1. Design the following using Op-Amp: *(Through Virtual Lab Link 1)*
 - a) A unity gain amplifier.
 - b) An inverting amplifier with a gain of “A”.
 - c) A non-inverting amplifier with a gain of “A”
2. Study and design Log and antilog amplifiers.
3. Voltage to current and current to voltage convertors.
4. Second order filters using operational amplifier for: *(Through Virtual Lab Link 1)*
 - a) Low pass filter of cutoff frequency 1 KHz.
 - b) High pass filter of frequency 12 KHz.
5. Realization of Band pass filter with unit gain of pass band from 1 KHz to 12 KHz.
6. Study and design voltage comparator and zero crossing detectors.
7. Function generator using operational amplifier (sine, triangular & square wave).
8. Design and construct astable multivibrator using IC 555 and
 - a) Plot the output waveform
 - b) Measure the frequency of oscillation *(Through Virtual Lab Link 2)*
9. Design and construct a monostable multivibrator using IC 555 and
 - a) Plot the output waveform
 - b) Measure the time delay *(Through Virtual Lab Link 2)*
10. Implement Schmitt Trigger Circuit using IC 555. *(Through Virtual Lab Link 2)*
11. Implement voltage-controlled oscillator using IC566 and plot the waveform. *(Through Virtual Lab Link 2)*
12. Study and design ramp generator using IC 566.

Virtual Lab Link:

1. <http://vlabs.iitkgp.ernet.in/be/exp17/index.html>
2. <http://hecoep.vlabs.ac.in/Experiment8/Theory.html?domain=ElectronicsandCommunications&lab=Hybrid%20Electronics%20Lab>

Available on: <http://www.vlab.co.in/broad-area-electronics-and-communications>

Course Outcomes:

At the end of this course students will demonstrate the ability to:

1. Design different non-linear applications of operational amplifiers such as log, antilog amplifiers and voltage comparators.
2. Explain and design different linear applications of operational amplifiers such as filters.
3. Demonstrate the function of waveforms generator using op-Amp.
4. Construct multivibrator and oscillator circuits using IC555 and IC566 and perform measurements of frequency and time.
5. Design and practically demonstrate the applications based on IC555 and IC566.

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| KEC-552 | MICROPROCESSOR & MICROCONTROLLER LAB | 0L:0T:2P | 1 Credit |
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SUGGESTIVE LIST OF EXPERIMENTS:

1. Write a program using 8085 Microprocessor for Decimal, Hexadecimal addition and subtraction of two Numbers. *(Through Virtual Lab Link)*
2. Write a program using 8085 Microprocessor for addition and subtraction of two BCD numbers. *(Through Virtual Lab Link)*
3. To perform multiplication and division of two 8 bit numbers using 8085. *(Through Virtual Lab Link)*
4. To find the largest and smallest number in an array of data using 8085 instruction set.
5. To write a program using 8086 to arrange an array of data in ascending and descending order. *(Through Virtual Lab Link)*
6. To convert given Hexadecimal number into its equivalent ASCII number and vice versa using 8086 instruction set.
7. To convert given Hexadecimal number into its equivalent BCD number and vice versa using 8086 instruction set.
8. To interface 8253 programmable interval timer and verify the operation of 8253 in six different modes.
9. To write a program to initiate 8251 and to check the transmission and reception of character.
10. Serial communication between two 8085 through RS-232 C port.
11. Write a program of Flashing LED connected to port 1 of the 8051 Micro Controller
12. Write a program to generate 10 kHz square wave using 8051.
13. Write a program to show the use of INT0 and INT1 of 8051.
14. Write a program for temperature & to display on intelligent LCD display.

Virtual Lab Link: http://vlabs.iitb.ac.in/vlabs-dev/labs_local/microprocessor/labs/explist.php

Available on: <http://www.vlab.co.in/broad-area-electronics-and-communications>

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Use techniques, skills, modern engineering tools, instrumentation and software/hardware appropriately to list and demonstrate arithmetic and logical operations on 8 bit data using microprocessor 8085.
2. Examine 8085 & 8086 microprocessor and its interfacing with peripheral devices.
3. State various conversion techniques using 8085 & 8086 and generate waveforms using 8085.
4. Implement programming concept of 8051 Microcontroller.
5. Design concepts to Interface peripheral devices with Microcontroller so as to design Microcontroller based projects.

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| KEC-553 | DIGITAL SIGNAL PROCESSING LAB | 0L:0T:2P | 1 Credit |
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SUGGESTIVE LIST OF EXPERIMENTS:

1. Introduction to MATLAB and or Open Source Software, Scilab (Using Spoken Tutorial MOOCs).
2. Write a Program for the generation of basic signals such as unit impulse, unit step, ramp, exponential, sinusoidal and cosine.
3. Implement IIR Butterworth analog Low Pass for a 4 KHz cut off frequency.
4. Verify Blackman and Hamming windowing techniques.
5. Evaluate 4-point DFT of and IDFT of $x(n) = 1, 0 \leq n \leq 3; 0$ elsewhere.
6. Verify Linear convolution of two sequences using FFT
7. Verify Circular Convolution of two sequences using FFT.
8. To verify FFT as sample interpolator.
9. To implement Tone Generation.
10. To implement floating point arithmetic.
11. To study about DSP Processors and architecture of TMS320C6713 DSP processor.
12. **VIRTUAL Lab by NME-ICT available at: (*Through Virtual Lab*)**
 - 12.1 Study of Discrete Fourier Transform (DFT) and its inverse.
 - 12.2 Study of FIR filter design using window method: Lowpass and highpass filter.
 - 12.3 Study of FIR filter design using window method: Bandpass and Bandstop filter.
 - 12.4 Study of Infinite Impulse Response (IIR) filter.

Virtual Lab Link: <http://vlabs.iitkgp.ernet.in/dsp/index.html#>
<http://vlabs.iitkgp.ernet.in/dsp/>

Available on: <http://www.vlab.co.in/broad-area-electronics-and-communications>

Spoken Tutorial (MOOCs):

Spoken Tutorial MOOCs, ' Course on Scilab', IIT Bombay (<http://spoken-tutorial.org/>)

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Create and visualize various discrete/digital signals using MATLAB/Scilab.
2. Implement and test the basic operations of Signal processing.
3. Examine and analyse the spectral parameters of window functions.
4. Design IIR and FIR filters for band pass, band stop, low pass and high pass filters.
5. Design the signal processing algorithms using MATLAB/Scilab.

**B.Tech 3rd Year
VI Semester
Syllabus**

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| KEC-601 | DIGITAL COMMUNICATION | 3L:1T:0P | 4 Credits |
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| Unit | Topics | Lectures |
|------|--|----------|
| I | Random Variables: Concept of Probability, Random variables, Statistical averages, Random process, Power Spectral Density & Autocorrelation Function of Random Processes, Gaussian Random Process. | 8 |
| II | Digital Communication Basics: Introduction to Digital communication systems, PSD of Line Coding schemes, Pulse shaping, Scrambling, Eye diagram, Gram-Schmidt orthogonalization scheme. | 8 |
| III | Digital Modulation: Modulation and Demodulation of Digital modulation schemes-ASK, FSK, PSK, DPSK, QPSK. Constellation diagram, Introduction to M-ary communication. | 8 |
| IV | Digital Receiver: Optimum threshold detection, Concept of Matched Filters, BER analysis of BASK, BFSK, BPSK, Introduction of Spread spectrum communication (DS-SS, FH-SS). | 8 |
| V | Information Theory: Measure of information-information, entropy, mutual information, mutual entropy, Source encoding (Shannon-Fano, Huffman), Shannon's channel capacity theorem, Introduction to error correction and detection, Linear block codes, Cyclic codes (systematic, non-systematic), Convolution coding and decoding. | 8 |

Text Books:

1. B.P. Lathi, "Modern Digital and Analog communication Systems", 4th Edition, Oxford University Press.
2. John G. Proakis, "Digital Communications", 5th Edition, TMH.
3. H. Taub, D L Schilling, Gautam Saha, "Principles of Communication", 4th Edition, TMH.
4. Singh & Sapray, Communication Systems, 3th Edition, TMH.

Reference Books:

1. Simon Haykin, "Communication Systems", 5th Edition, Wiley India.
2. (Schaum's Outline Series) H P HSU & D Mitra, "Analog and Digital Communications", TMH, 3rd Edition.

Course Outcomes: At the end of this course students will demonstrate the ability:

1. To formulate basic statistics involved in communication theory.
2. To demonstrate the concepts involved in digital communication.
3. To explain the concepts of digital modulation schemes.
4. To analyze the performance of digital communication systems.
5. To apply the concept of information theory in digital systems.

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| KEC-602 | Control System | 3L:1T:0P | 4 Credits |
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| Unit | Topics | Lectures |
|-------------|---|-----------------|
| I | Introduction to Control Systems: Basic Components of a control system, Feedback and its effect, types of feedback control systems. Block diagrams Reduction and signal flow graphs, Modeling of Physical systems: electrical networks, mechanical systems elements, free body diagram, analogous Systems, sensors and encoders in control systems, modeling of armature controlled and field controlled DC servomotor. | 8 |
| II | State-Variable Analysis: Introduction, vector matrix representation of state equation, state transition matrix, state-transition equation, relationship between state equations and high-order differential equations, relationship between state equations and transfer functions, Decomposition of transfer functions, Controllability and observability, Eigen Value and Eigen Vector, Diagonalization. | 8 |
| III | Time domain Analysis of Control Systems: Time response of continuous data systems, typical test signals for the time response of control systems, unit step response and time-domain specifications, time response of a first order system, transient response of a prototype second order system, Steady-State error, Static and dynamic error coefficients, error analysis for different types of systems. | 8 |
| IV | Stability of Linear Control Systems: Bounded-input bounded-output stability continuous data systems, zero-input and asymptotic stability of continuous data systems, Routh Hurwitz criterion, Root-Locus Technique: Introduction, Properties of the Root Loci, Design aspects of the Root Loci. | 8 |
| V | Frequency Domain Analysis: Resonant peak and Resonant frequency, Bandwidth of the prototype Second order system, effects of adding a zero to the forward path, effects of adding a pole to the forward path, polar plot, Nyquist stability criterion, stability analysis with the Bode plot, relative stability: gain margin and phase margin. | 8 |

Text Book:

1. I. J. Nagrath & M. Gopal, "Control System Engineering", 6th Ed. New Age International Publishers, 2018
2. B.C. Kuo & Farid Golnaraghi, "Automatic Control Systems", 9th Edition, John Wiley India, 2008

Reference Books:

1. (Schaums Outlines Series) Joseph J. Distefano III, Allen R. Stubberud, Ivan J. Williams, "Control Systems", 3rd Edition, TMH, Special Indian Edition, 2010.
2. A. Anand Kumar, "Control Systems", Second Edition, PHI Learning private limited, 2014.
3. William A. Wolovich, "Automatic Control Systems", Oxford University Press, 2011.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Describe the basics of control systems along with different types of feedback and its effect. Additionally they will also be able to explain the techniques such as block diagrams reduction, signal flow graph and modelling of various physical systems along with modelling of DC servomotor.
2. Explain the concept of state variables for the representation of LTI system.
3. Interpret the time domain response analysis for various types of inputs along with the time domain specifications.
4. Distinguish the concepts of absolute and relative stability for continuous data systems along with different methods.
5. Interpret the concept of frequency domain response analysis and their specifications.

ELECTRONICS AND COMMUNICATION ENGINEERING

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| KEC-603 | Antenna & Wave Propagation | 3L:1T:0P | 4 Credits |
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| Unit | Topics | Lectures |
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| I | Coordinate Systems and Transformation: Cartesian, Cylindrical, Spherical transformation. Vector calculus: Differential length, area and volume, line, surface and volume integrals, Del operator, Gradient, Divergence of a vector, Divergence theorem, Curl of a vector, Stokes's theorem, Laplacian of a scalar. | 6 |
| II | Electrostatic fields and Magnetostatic fields: Electric field intensity, Electric field due to charge distribution, Electric flux density, Gauss's Law- Maxwell's equation, Continuity equation and relaxation time, boundary conditions, Magneto-static fields, Ampere's circuit law, Maxwell's equation, magnetic scalar and vector potential, Magnetic boundary conditions, Maxwell's equation in final form. | 10 |
| III | Antenna fundamental and definitions: Introduction, Basic antenna parameters, Patterns, Beam area (or Beam solid angle) ΩA , Radiation intensity, Beam efficiency, Directivity D and Gain G, Directivity and resolution, Antenna apertures, Effective height, The radio communication link, Fields from oscillating dipole, Single-to-noise ratio (SNR), Antenna temperature, Antenna impedance. | 8 |
| IV | Antenna Design: Electric dipoles, The short electric dipole, The fields of a short dipole, Radiation resistance of short electric dipole, Thin linear antenna, Radiation resistance of $\lambda/2$ antenna, Array of two driven $\lambda/2$ elements: Broadside case and end-fire case, Horizontal antennas above a plane ground, Vertical antennas above a plane ground, Yagi-Uda antenna design, Longwire antennas, Folded dipole antennas. | 8 |
| V | Wave Propagation: Plane earth reflection, Space wave and surface wave. Space wave propagation: Introduction, Field strength relation, Effects of imperfect earth, Effects of curvature of earth. Sky wave propagation: Introduction structural, details of the ionosphere, Wave propagation mechanism, Refraction and reflection of sky waves by ionosphere, Ray path, Critical frequency, MUF, LUF, OF, Virtual height and skip distance, Relation between MUF and the skip distance, Multi-Hop propagation, Wave characteristics. | 8 |

Text Books:

1. MNO Sadiku, "Elements of Electromagnetic", 7th Ed, Oxford University Press, 2018.
2. John D Kraus, Ronald J Marhefka and Ahmad S. Khan, "Antennas and Wave Propagation", 5th Edition, Tata McGraw Hill, 2017.
3. Das, Antennas and Wave Propagation, TMH 1st Edition.
4. C. A. Balanis, "Antenna Theory Analysis and Design", John Wiley, 2016.
5. WH Hayt and JA Buck, "Engineering Electromagnetic", 7th Edition TMH, 2013.
6. (Schaums Outlines Series) Joseph J. Distefano III, Allen R. Stubberud, Ivan J. Williams, "Engineering Electromagnetic", 3rd Edition, TMH, Special Indian Edition, 2010.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Identify different coordinate systems and their applications in electromagnetic field theory to establish a relation between any two systems using the vector calculus.
2. Explain the concept of static electric field, current and properties of conductors.
3. Express the basic concepts of ground, space, sky wave propagation mechanism.
4. Demonstrate the knowledge of antenna fundamentals and radiation mechanism of the antenna.
5. Analyze and design different types of basic antennas.

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| KEC-061 | MICROCONTROLLER & EMBEDDED SYSTEMS DESIGN | 3L:0T:0P | 3 Credits |
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| Unit | Topics | Lectures |
|------|--|----------|
| I | Advanced concepts in 8051 architecture: Review of 8051 architecture, concept of synchronous serial communication, SPI and I2C communication protocols, study of SPI port on 89LP 51RD2, study of SAR ADC/DAC MCP3304 / MCP 33, interfacing concepts for SPI based ADC/DAC, study of watchdog timer, study of PCA timer in different modes like capture mode, PWM generation mode, High speed output toggle mode Embedded 'C' programming for the above peripherals Introduction, AVR Family architecture , Register File, The ALU. Memory access and Instruction execution. I/O memory. EEPROM. I/O ports. Timers. Interrupt Structure | 8 |
| II | MSP430x5x Microcontroller: series block diagram, address space, on-chip peripherals (analog and digital), and Register sets. Instruction set, instruction formats, and various addressing modes of 16-bit microcontroller; Sample embedded system on MSP430 microcontroller. Memory Mapped Peripherals, programming System registers, I/O pin multiplexing, pull up/down registers, GPIO control. Interrupts and interrupt programming. | 8 |
| III | Peripheral Devices: Watch dog timer, system clocks, Timer & Real Time Clock (RTC), PWM control, timing generation and measurements. Analog interfacing and data acquisition ADC and Comparator in MSP430, data transfer using DMA. | 8 |
| IV | Serial communication basics, Synchronous/Asynchronous interfaces (like UART, USB, SPI, and I2C). UART protocol, I2C protocol, SPI protocol. Implementing and programming UART, I2C, SPI interface using MSP430, Interfacing external devices. | 8 |
| V | Internet of Things (IoT): overview and architecture, Overview of wireless sensor networks and design examples. Various wireless connectivity: NFC, ZigBee, Bluetooth, Bluetooth Low Energy, Wi-Fi. Adding Wi-Fi capability to the Microcontroller, Embedded Wi-Fi, User APIs for Wireless and Networking applications, Building IoT applications using CC3100 user API for connecting sensors. | 8 |

Text Books:

1. Mazidi Ali Muhammad, Mazidi Gillispie Janice, and Mc Kinlay Rolin D “The 8051 Microcontroller and Embedded Systems using Assembly and C”, Pearson Publication,2006
2. John H Davies, “MSP430 Microcontroller Basics” Newnes Publication,2008

Reference Books:

1. TI MSP430x5xx and MSP430x6xx Family User's Guide , Revised 2018.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Explain the advance concept of 8051 architectures and AVR family architecture and compare them for different applications.
2. To demonstrate the basics of MSP430x5x Microcontroller
3. To execute the I/O interfacing and peripheral devices associated with Microcontroller SoC (system on chip).
4. Evaluate the data transfer information through serial & parallel ports and implement its interfacing with MSP430.
5. Demonstrate the basics of IoT, WSN and its application sectors and design IoT based

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projects using MSP430 microcontroller.

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| KEC-062 | SATELLITE COMMUNICATION | 3L:0T:0P | 3 Credits |
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| Unit | Topics | Lectures |
|-------------|--|-----------------|
| I | Introduction to Satellite Communication: History, Overview of Satellite Communication, Types of Satellite, Types of Orbit, Satellite services, Advantages & Applications of Satellite communication, Satellite Life phases, Space Debris, Introduction to Geo-synchronous and Geo-stationary satellites. | 8 |
| II | Orbital Mechanics: Orbital Mechanics, Kepler's Three laws of Planetary Motion, Developing the Equations of the orbit, Look Angle Determination, Earth Stations, Orbital Perturbations, Orbital effects in Communication system performance. | 8 |
| III | Satellite Sub-systems: Seven segments of Satellite communication, Attitude and Orbit control systems, Telemetry, Tracking and command control system, Power supply system. Satellite Link Design: Basic transmission theory, System noise temperature and G/T ratio, Design of down link and uplink, Design of satellite links for specified C/N. | 8 |
| IV | Introduction to Various Satellite Systems: VSAT, Direct broadcast satellite television and radio, Satellite navigation and the Global positioning systems, GPS position location principle, GPS receivers and codes, Satellite Signal Acquisition, GPS navigation Message, GPS Signal Levels, Timing Accuracy, GPS Receiver Operation. | 8 |
| V | Launchers & Advanced Technologies: Mechanism of Satellite launching, Launch Vehicles, Advanced launching tech like Space X, Intelligent Testing, Control and Decision making for Space, Inter Satellite Link. Indian Satellite Systems: History and Overview of Indian Satellite System, Achievements, GSLV, PSLV, Advanced Technology Vehicle. | 8 |

Text Books:

1. B.Pratt, A.Bostian, "Satellite Communications", Wiley India, 2nd Edition, 2006.
2. D. Roddy, "Satellite Communications", TMH, 4th Edition, 2001.
3. Digital Satellite Communications/ Tri T. Ha./ McGraw-Hill, 2nd Edition
4. D.C. Agrawal, Satellite communication, Khanna Publishers; 7th Edition.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Define and list the benefits of satellite communication.
2. Demonstrate orbital mechanics principles of satellite communication systems and solve problems related to it.
3. Describe a satellite link and identify ways to improve the link performance.
4. Classify new technologies of satellite communication systems as per given specifications.
5. Examine advanced technologies of satellite launching and describe the Indian satellite system.

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| KEC-063 | DATA COMMUNICATION NETWORKS | 3L:0T:0P | 3 Credits |
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| Unit | Topics | Lectures |
|-------------|--|-----------------|
| I | Introduction to Networks & Data Communications: Goals and Applications of Networks ,The Internet, Protocols & Standards, Layered Tasks, OSI reference Model, TCP / IP, Addressing, Line Coding Review. | 8 |
| II | Physical Layer: Transmission Media- Guided and unguided, Network Topology Design, Data Link Layer: Error detection and Correction, Framing, Flow and Error Control Protocols, Noiseless Channel and Noisy Channel Protocol, HDLC, Point-to-Point Protocol | 8 |
| III | Multiple Access: RANDO, CDMA, CSMA/CD, CSMA/CA, Controlled Access, Channelization Wired LANs: IEEE Standards, Standard Ethernet, Fast Ethernet, Gigabit Ethernet, Wireless LAN IEEE 802.11, Bluetooth IEEE 802.16. | 8 |
| IV | Network Layer: Design Issues. Routing Algorithms. Congestion control Algorithms. Internetworking –TCP/IP, IP Packet, IPv4 and IPv6 Protocols, IPV4 Addresses, Connecting Devices, Virtual LAN IPV6 Addresses. | 8 |
| V | Transport Layer Protocol: UDP and TCP, ATM, Cryptography, Network Security, Session Layer-Design issues. Application Layer: File Transfer, Electronic mail, HTTP, WWW, SMTP, Cryptography, Network Security. | 8 |

Text Books:

1. B. A. Forouzan, “Data Communications and Networking”, 5th Edition, TMH, 2017.

Reference Books:

1. S. Tanenbaum, “Computer Networks”, 4th Edition, Pearson, 2013.
2. W. Stallings, “Data and Computer Communication”, 8th Edition, Pearson, 2007.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Identify the issues and challenges in the architecture of a network.
2. Analyze the services and features of various protocol layers in data layer.
3. Demonstrate the knowledge of multiple access to design a access technique for a particular application.
4. Realize protocols at different layers of a network hierarchy.
5. Recognize security issues in a network and various application of application layer.

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| KEC-062 | ANALOG SIGNAL PROCESSING | 3L : 0T : 0P | 3 Credits |
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| Unit | Topics | Lectures |
|-------------|--|-----------------|
| I | Introduction to domains and the analogue/digital trade off, Introduction to current conveyor, current feedback amplifier. Analog signal filtering: introduction to bilinear transfer functions and active realizations. Second-order filter realization, filter design parameters (Q and ω_0), frequency response, Three op-amp biquad, effect of finite gain of op-amp over filters, Sallen-Key biquad. | 8 |
| II | Ideal low-pass filter, Butterworth and Chebyshev magnitude response, pole locations, low-pass filter specifications, comparison of Maximally flat and Equal ripple responses. | 8 |
| III | Delay equalization: equalization procedures, equalization with first-order and second order modules, strategies for equalization design. Definition of Bode sensitivity. | 8 |
| IV | The General Impedance Convertor (GIC), optimal design of the GIC, realization of simple ladders, Gorski-Popiel's Embedding Technique, Bruton's FDNR technique, Creating negative components. | 8 |
| V | Elementary transconductor building blocks, resistors, integrators, amplifiers, summers, Gyrator, First and second order filters, Higher order filters | 8 |

Text Book:

1. R. Schaumann and M.E. Valkenberg, "Design of Analog Circuits", Oxford University Press

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Describe and apply fundamentals of signal processing in analog domain and its associated concepts like OTA and current conveyor.
2. Introduction of filter and its designing parameters
3. Solve problems and design higher order filters like Butterworth and Chebyshev.
4. Understand and explain the reasons for delay in filter designing and its procedure to equalize.
5. Understand the principles of the inductor simulation like general impedance convertor (GIC), optimal design of the GIC, Gorski-Popiel's Embedding Technique, Bruton's FDNR technique which are used for placing equivalent inductor on integrated circuits.

ELECTRONICS AND COMMUNICATION ENGINEERING

| | | | |
|----------------|--|-----------------|------------------|
| KEC-065 | RANDOM VARIABLES & STOCHASTIC PROCESS | 3L:0T:0P | 3 Credits |
|----------------|--|-----------------|------------------|

| Unit | Topics | Lectures |
|------|---|----------|
| I | Probability: Introduction to set theory, experiments and sample spaces, joint probability, conditional probability, concept of total Probability, Bayes' Theorem, and independent events, Bernoulli's trials, combined experiments. | 8 |
| II | Random Variables: Introduction, types of random variables, cumulative distribution function and probability density functions, Standard distributions: Gaussian, exponential, Rayleigh, uniform, Bernoulli, binomial, Poisson, discrete uniform and conditional distributions. Functions of one random variable: distribution, mean, variance, moments and characteristics functions. | 8 |
| III | Multiple Random Variables: Joint distributions, joint density function and properties, marginal distribution and density functions, conditional distribution and density Functions, statistical independence, functions of two random variables, joint moments, Multiple random variables: multiple functions of multiple random variables, jointly Gaussian random variables, sums of random variable, Central limit theorem. | 8 |
| IV | Stochastic Processes: Definitions, Random process concept, Statistics of stochastic processes: Mean, Autocorrelation, Covariance Functions and its properties, Strict and Wide sense stationary, random processes, Time Averages and Ergodicity, Mean-Ergodic Processes. | 8 |
| V | Stochastic Processes in Frequency Domain: Power spectrum of stochastic processes, Properties of power spectral density, Relationship between Power Spectrum and Autocorrelation Function, the Cross-Power Density Spectrum and Properties, Relationship between Cross-Power Spectrum and Cross-Correlation Function, Transmission over LTI systems, Gaussian and White processes. | 8 |

Text Books:

1. Probability, Random Variables And Stochastic Processes, Papoulis, TMH (2002)
2. Stochastic Processes, 2ed, Ross, Wiley.(1996)

Reference Books:

1. Devore – Probability and statistics for engineering and sciences, Cengage learning 2011
2. Mendenhall – Introduction to probability and statistics, Cengage learning 2012
3. Probability, Random Variables And Random Signal Principles, Peebles, TMH 2002
4. Probability Theory and Stochastic Processes for Engineers, Bhat, Pearson 2011
5. Probability and Random Processes with Application to Signal Processing, 3/e, Stark, Pearson 2002
6. Random Variables & Stochastic Processes, Gaur and Srivastava, Genius publications 2003
7. Random Processes: Filtering, Estimation and Detection, Ludeman, Wiley 2002
8. An Introduction to Probability Theory & Its App., Feller, Wiley 1969

Course Outcomes:

At the end of this course students will demonstrate the ability to:

1. Students will be able to explain the basic learning of Probability.
2. Students will be able to demonstrate the concept of Random Variables.
3. Students will be able to analyze Multiple Random Variables.
4. Students will be able to interpret the basics of Stochastic Processes.
5. Students will be able to express Stochastic Processes in Frequency domain.

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| KEC-651 | DIGITAL COMMUNICATION LAB | 0L:0T:2P | 1 Credit |
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SUGGESTIVE LIST OF EXPERIMENTS:

Part A

1. To study Eye diagram patterns of various digital pulses.
2. To study the inter symbol interference.
3. To study generation of Unipolar RZ & NRZ Line Coding.
4. To study generation of Polar RZ & NRZ Line Coding.
5. To study generation of Bipolar RZ & NRZ Line Coding.
6. Implementation and analysis of BASK modulation and demodulation
7. Implementation and analysis of BFSK modulation and demodulation
8. Implementation and analysis of BPSK modulation and demodulation. *(Through Virtual Lab)*
9. Implementation and analysis of QPSK modulation and demodulation. *(Through Virtual Lab)*
10. To simulate M-ary Phase shift keying technique using MATLAB.
11. To study generation and detection of DPSK using MATLAB.
12. Implementation and analysis of Delta modulation and demodulation.
13. Implementation and analysis of DSSS Modulation, Demodulation & BER measurement.
14. Implementation and analysis of FHSS Modulation, Demodulation & BER measurement.
15. To study encoding and decoding of Linear Block Codes
16. To study the working of Convolution encoder.

Part B

1. To study simple dipole $\lambda/2$ antenna and to calculate beam-width, front / back ratio, and gain of the antenna.
2. To study folded dipole antenna and to calculate beam-width, front / back ratio, and gain of the antenna.
3. To study $\lambda/2$ phase array end-fire antenna and to calculate beam-width, front / back ratio, and gain of the antenna.
4. To study broadside array antenna and to calculate beam-width, front / back ratio, and gain of the antenna.

Virtual Lab Link: <https://vlab.amrita.edu/?sub=1&brch=201>

Course Outcomes: At the end of this course students will demonstrate the ability:

1. To formulate basic concepts of pulse shaping in digital communication.
2. To identify different line coding techniques and demonstrate the concepts.
3. To design equipments related to digital modulation and demodulation schemes.
4. To analyze the performance of various digital communication systems and evaluate the key parameters.
5. To conceptualize error detection & correction using different coding schemes in digital communication.

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| KEC-652 | CONTROL SYSTEM LAB | 0L:0T:2P | 1 Credit |
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SUGGESTIVE LIST OF EXPERIMENTS:

1. Introduction to MATLAB Control System Toolbox.
2. Determine transpose, inverse values of given matrix.
3. Plot the pole-zero configuration in s-plane for the given transfer function.
4. Determine the transfer function for given closed loop system in block diagram representation.
5. Create the state space model of a linear continuous system.
6. Determine the State Space representations of the given transfer function.
7. Determine the time response of the given system subjected to any arbitrary input.
8. Plot unit step response of given transfer function and find delay time, rise time, peak time, peak overshoot and settling time.
9. Determine the steady state errors of a given transfer function.
10. Plot root locus of given transfer function, locate closed loop poles for different values of k.
11. Plot bode plot of given transfer function. Also determine gain and phase margins.
12. Plot Nyquist plot for given transfer function. Also determine the relative stability by measuring gain and phase margin.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Classify different tools in MATLAB along with the basic matrix operations used in MATLAB.
2. Evaluate the poles and zeros on s-plane along with transfer function of a given system.
3. Construct state space model of a linear continuous system.
4. Evaluate the various specifications of time domain response of a given system.
5. Appraise the steady state error of a given transfer function.
6. Examine the relative stability of a given transfer function using various methods such as root locus, Bode plot and Nyquist plot.

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| KEC-653 | CAD FOR ELECTRONICS LAB | 0L:0T:2P | 1 Credit |
|---------|-------------------------|----------|----------|

SUGGESTIVE LIST OF EXPERIMENTS:

Part A

PSPICE Experiments:

- (a) Transient Analysis of BJT inverter using step input.
(b) DC Analysis (VTC) of BJT inverter
- (a) Transient Analysis of NMOS inverter using step input.
(b) Transient Analysis of NMOS inverter using pulse input.
(c) DC Analysis (VTC) of NMOS inverter.
- (a) Analysis of CMOS inverter using step input.
(b) Transient Analysis of CMOS inverter using step input with parameters.
(c) Transient Analysis of CMOS inverter using pulse input.
(d) Transient Analysis of CMOS inverter using pulse input with parameters.
(e) DC Analysis (VTC) of CMOS inverter with and without parameters.
- Transient & DC Analysis of NAND Gate using CMOS inverter.
- Transient Analysis of NOR Gate inverter and implementation of XOR gate using NOR gate
- To design and perform transient analysis of D latch using CMOS inverter.
- To design and perform the transient analysis of SR latch circuit using CMOS inverter.
- To design and perform the transient analysis of CMOS transmission gate.
- Analysis of frequency response of Common Source amplifiers.
- Analysis of frequency response of Source Follower amplifiers

Part B :

HDL (using VHDL program module & verilog Module)

VHDL PROGRAMS

- Design and Simulation of Full Adder using VHDL program module
- Design and Simulation of 4x1 MUX using VHDL program module
- Design and Simulation of BCD to Excess-3 code using VHDL program module
- Design and Simulation of 3 to 8 decoder using VHDL program module
- Design and Simulation of JK Flip-flop using VHDL program module
- Design and Simulation of CMOS Inverter using verilog Module

Course Outcomes: At the end of this course students will demonstrate the ability to:

- Design and analyze the performance of different type of inverters.
- Design and analyze the performance of the basic logic gates using CMOS inverter circuit.
- Design and analyze the performance of the memory based digital circuits using CMOS inverter circuit.
- Analyze the performance of the different configuration of MOS amplifier circuits.

B.Tech.
V & VI Semester

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|---|-------------------|---|
| 1 | KNC501/ KNC601 | CONSTITUTION OF INDIA, LAW AND ENGINEERING |
|---|-------------------|---|

Module 1--Introduction and Basic Information about Indian Constitution:

Meaning of the constitution law and constitutionalism, Historical Background of the Constituent Assembly, Government of India Act of 1935 and Indian Independence Act of 1947, Enforcement of the Constitution, Indian Constitution and its Salient Features, The Preamble of the Constitution, Fundamental Rights, Fundamental Duties, Directive Principles of State Policy, Parliamentary System, Federal System, Centre-State Relations, Amendment of the Constitutional Powers and Procedure, The historical perspectives of the constitutional amendments in India, Emergency Provisions: National Emergency, President Rule, Financial Emergency, and Local Self Government – Constitutional Scheme in India.

Module 2-Union Executive and State Executive:

Powers of Indian Parliament Functions of Rajya Sabha, Functions of Lok Sabha, Powers and Functions of the President, Comparison of powers of Indian President with the United States, Powers and Functions of the Prime Minister, Judiciary – The Independence of the Supreme Court, Appointment of Judges, Judicial Review, Public Interest Litigation, Judicial Activism, LokPal, Lok Ayukta, The Lokpal and Lok ayuktas Act 2013, State Executives – Powers and Functions of the Governor, Powers and Functions of the Chief Minister, Functions of State Cabinet, Functions of State Legislature, Functions of High Court and Subordinate Courts.

Module 3- Introduction and Basic Information about Legal System:

The Legal System: Sources of Law and the Court Structure: Enacted law -Acts of Parliament are of primary legislation, Common Law or Case law, Principles taken from decisions of judges constitute binding legal rules. The Court System in India and Foreign Courtiers (District Court, District Consumer Forum, Tribunals, High Courts, Supreme Court). Arbitration: As an alternative to resolving disputes in the normal courts, parties who are in dispute can agree that this will instead be referred to arbitration. Contract law, Tort, Law at workplace.

Module 4- Intellectual Property Laws and Regulation to Information:

Intellectual Property Laws: Introduction, Legal Aspects of Patents, Filing of Patent Applications, Rights from Patents, Infringement of Patents, Copyright and its Ownership, Infringement of Copyright, Civil Remedies for Infringement, Regulation to Information-Introduction, Right to Information Act, 2005, Information Technology Act, 2000, Electronic Governance, Secure Electronic Records and Digital Signatures, Digital Signature Certificates, Cyber Regulations Appellate Tribunal, Offences, Limitations of the Information Technology Act.

Module 5 -Business Organizations and E-Governance:

Sole Traders, Partnerships: Companies: The Company's Act: Introduction, Formation of a Company, Memorandum of Association, Articles of Association, Prospectus, Shares, Directors, General Meetings and Proceedings, Auditor, Winding up.

E-Governance and role of engineers in E-Governance, Need for reformed engineering serving at the Union and State level, Role of I.T. professionals in Judiciary, Problem of Alienation and Secessionism in few states creating hurdles in Industrial development.

COURSE OBJECTIVE:

- To acquaint the students with legacies of constitutional development in India and help those to understand the most diversified legal document of India and philosophy behind it.
- To make students aware of the theoretical and functional aspects of the Indian Parliamentary System.
- To channelize students' thinking towards basic understanding of the legal concepts and its implications for engineers.
- To acquaint students with latest intellectual property rights and innovation environment with related regulatory framework.
- To make students learn about role of engineering in business organizations and e-governance.

COURSE OUTCOME: At the end of the course, learners should be able to-

1. Identify and explore the basic features and modalities about Indian constitution.
2. Differentiate and relate the functioning of Indian parliamentary system at the center and state level.
3. Differentiate different aspects of Indian Legal System and its related bodies.
4. Discover and apply different laws and regulations related to engineering practices.
5. Correlate role of engineers with different organizations and governance models

Pedagogy: Lecture, Problem based learning, Group discussions, Visual media, Films, Documentaries, Debate forums.

Suggested Readings:

- Brij Kishore Sharma: *Introduction to the Indian Constitution*, 8th Edition, PHI Learning Pvt. Ltd.
- Granville Austin: *The Indian Constitution: Cornerstone of a Nation (Classic Reissue)*, Oxford University Press.
- Subhash C. Kashyap: *Our Constitution: An Introduction to India's Constitution and constitutional Law*, NBT, 2018.
- Madhav Khosla: *The Indian Constitution*, Oxford University Press.
- PM Bakshi: *The Constitution of India*, Latest Edition, Universal Law Publishing.
- V.K. Ahuja: *Law Relating to Intellectual Property Rights* (2007)
- Suresh T. Viswanathan: *The Indian Cyber Laws*, Bharat Law House, New Delhi-88
- P. Narayan: *Intellectual Property Law*, Eastern Law House, New Delhi
- Prabudh Ganguli: *Gearing up for Patents: The Indian Scenario*, Orient Longman.
- BL Wadehra: *Patents, Trademarks, Designs and Geographical Indications Universal Law Publishing - LexisNexis*.
- *Intellectual Property Rights: Law and Practice, Module III* by ICSI (only relevant sections)
- Executive programme study material Company Law, Module II, by ICSI (The Institute of Companies Secretaries of India) (Only relevant sections i.e., Study 1, 4 and 36). <https://www.icsi.edu/media/webmodules/publications/Company%20Law.pdf>
- Handbook on e-Governance Project Lifecycle, Department of Electronics & Information Technology, Government of India, https://www.meity.gov.in/writereaddata/files/e-Governance_Project_Lifecycle_Participant_Handbook-5Day_CourseV1_20412.pdf
- Companies Act, 2013 Key highlights and analysis by PWC. <https://www.pwc.in/assets/pdfs/publications/2013/companies-act-2013-key-highlights-and-analysis.pdf>

Referred Case Studies:

- Keshavanand Bharati V. State of Kerala, AIR 1973 SC 1461.
- Maneka Gandhi V. Union of India AIR, 1978 SC 597.
- S.R. Bammai V. Union of India, AIR 1994 SC 1918.
- Kuldip Nayyar V. Union of India, AIR 2006 SC312.
- A.D.M. Jabalpur V. ShivkantShakla, AIR 1976 SC1207.
- Remshwar Prasad V. Union of India, AIR 2006 SC980.
- Keshav Singh in re, AIR 1965 SC 745.
- Union of India V. Talsiram, AIR 1985 SC 1416.
- Atiabari Tea Estate Co.V. State of Assam, AIR 1961SC232.
- SBP & Co. Vs. Patel Engg. Ltd. 2005 (8) SCC 618.
- Krishna Bhagya Jala Nigam Ltd. Vs. G. Arischandra Reddy (2007) 2 SCC 720.
- Oil & Natural Gas Corporation Vs. Saw Pipes Ltd. 2003 (4) SCALE 92 – 185.

**** (Other relevant case studies can be consulted by the teacher as per the topic).**

Prescribed Legislations:

1. Information Technology Act, 2000 with latest amendments.
2. RTI Act 2005 with latest amendments.
3. Information Technology Rules, 2000
4. Cyber Regulation Appellate Tribunal Rules, 2000

Suggested aid for Students and Pedagogic purpose

- RSTV debates on corporate law, IPR and patent issues
- NPTEL lectures on IPR and patent rights

Episodes of 10 -part mini TV series “Samvidhan: The Making of Constitution of India” by RSTV.

B.Tech.
V & VI Semester

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|---|-------------------|--|
| 2 | KNC502/ KNC602 | INDIAN TRADITION, CULTURE AND SOCIETY |
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INDIAN TRADITIONS, CULTURAL AND SOCIETY

L: T:P: 2: 0:0

Module 1- Society State and Polity in India

State in Ancient India: Evolutionary Theory, Force Theory, Mystical Theory Contract Theory, Stages of State Formation in Ancient India, Kingship , Council of Ministers Administration Political Ideals in Ancient India Conditions' of the Welfare of Societies, The Seven Limbs of the State, Society in Ancient India, Purusārtha, Varnāshrama System, Āshrama or the Stages of Life, Marriage, Understanding Gender as a social category, The representation of Women in Historical traditions, Challenges faced by Women. Four-class Classification, Slavery.

Module 2- Indian Literature, Culture, Tradition, and Practices

Evolution of script and languages in India: Harappan Script and Brahmi Script. The Vedas, the Upanishads, the Ramayana and the Mahabharata, Puranas, Buddhist And Jain Literature in Pali,Prakrit And Sanskrit, Kautilya's Arthashastra, Famous Sanskrit Authors, Telugu Literature, Kannada Literature, Malayalam Literature ,Sangama Literature Northern Indian Languages & Literature, Persian And Urdu ,Hindi Literature

Module 3- Indian Religion, Philosophy, and Practices

Pre-Vedic and Vedic Religion, Buddhism, Jainism, Six System Indian Philosophy, Shankaracharya, Various Philosophical Doctrines , Other Heterodox Sects, Bhakti Movement, Sufi movement, Socio religious reform movement of 19th century, Modern religious practices.

Module 4-Science, Management and Indian Knowledge System

Astronomy in India, Chemistry in India, Mathematics in India, Physics in India, Agriculture in India, Medicine in India ,Metallurgy in India, Geography, Biology, Harappan Technologies, Water Management in India, Textile Technology in India ,Writing Technology in India Pyrotechnics in India Trade in Ancient India/,India's Dominance up to Pre-colonial Times

Module 5- Cultural Heritage and Performing Arts

Indian Architect, Engineering and Architecture in Ancient India, Sculptures, Seals, coins, Pottery, Puppetry, Dance, Music, Theatre, drama, Painting, Martial Arts Traditions, Fairs and Festivals, Current developments in Arts and Cultural, Indian's Cultural Contribution to the World. Indian Cinema

COURSE OBJECTIVES:

- The course aims at imparting basic principles of thought process, reasoning and inference to identify the roots and details of some of the contemporary issues faced by our nation and try to locate possible solutions to these challenges by digging deep into our past.
- To enable the students to understand the importance of our surroundings and encourage the students to contribute towards sustainable development.
- To sensitize students towards issues related to 'Indian' culture, tradition and its composite character.

- To make students aware of holistic life styles of Yogic-science and wisdom capsules in Sanskrit literature that are important in modern society with rapid technological advancements and societal disruptions.
- To acquaint students with Indian Knowledge System, Indian perspective of modern scientific world-view and basic principles of Yoga and holistic health care system.

COURSE OUTCOMES: Ability to understand, connect up and explain basics of Indian Traditional knowledge modern scientific perspective.

Suggested Pedagogy for Teachers

- Project based learning
- Case studies
- Group discussion
- Presentations

Suggested Text & Reference Books

1. V. Sivaramakrishna (Ed.), *Cultural Heritage of India-Course Material*, Bharatiya Vidya Bhavan, Mumbai, 5th Edition, 2014
2. S. Baliyan, *Indian Art and Culture*, Oxford University Press, India
3. Swami Jitatanand, *Modern Physics and Vedant*, Bharatiya Vidya Bhavan
4. Romila Thapar, *Readings In Early Indian History* Oxford University Press , India
5. Fritz of Capra, *Tao of Physics*
6. Fritz of Capra, *The wave of Life*
7. V N Jha (English Translation), *Tarkasangraha of Annam Bhatta*, International Chinmay Foundation, Velliarnad, Amaku, am
8. *Yoga Sutra of Patanjali*, Ramakrishna Mission, Kolkatta
9. GN Jha (Eng. Trans.) Ed. R N Jha, *Yoga-darshanam with Vyasa Bhashya*, Vidyanidhi Prakasham, Delhi, 2016
10. RN Jha, *Science of Consciousness Psychotherapy and Yoga Practices*, Vidyanidhi Prakasham, Delhi, 2016
11. P R Sharma (English translation), *Shodashang Hridayam*
12. Basham, A.L., *The Wonder that was India* (34th impression), New Delhi, Rupa & co
13. Sharma, R.S., *Aspects of Political Ideas and Institutions in Ancient India*(fourth edition), Delhi, Motilal Banarsidass,

B.Tech. VI Semester

OPEN ELECTIVE-I

| | |
|---------|---|
| KOE-061 | REAL TIME SYSTEMS |
| KOE-062 | EMBEDDED SYSTEM |
| KOE-063 | INTRODUCTION TO MEMS |
| KOE-064 | OBJECT ORIENTED PROGRAMMING |
| KOE-065 | NUMERICAL TECHNIQUES |
| KOE066 | GIS & REMOTE SENSING |
| KOE-067 | UNDERSTANDING THE HUMAN BEING COMPREHENSIVELY- HUMAN ASPIRATIONS AND ITS FULFILLMENT |

KOE-061 REAL TIME SYSTEMS

| Unit | Topics | Lectures |
|------|--|----------|
| I | Introduction Definition, Typical Real Time Applications: Digital Control, High Level Controls, Signal Processing etc., Release Times, Dead-lines, and Timing Constraints, Hard Real Time Systems and Soft Real Time Systems, Reference Models for Real Time Systems: Processors and Resources, Temporal Parameters of Real Time Workload, Periodic Task Model, Precedence Constraints and Data Dependency. | 8 |
| II | Real Time Scheduling Common Approaches to Real Time Scheduling: Clock Driven Approach, Weighted Round Robin Approach, Priority Driven Approach, Dynamic Versus Static Systems, Optimality of Effective-Deadline-First (EDF) and Least-Slack-Time-First (LST) Algorithms, Rate Monotonic Algorithm, Offline Versus Online Scheduling, Scheduling Aperiodic and Sporadic jobs in Priority Driven and Clock Driven Systems. | 8 |
| III | Resources Sharing Effect of Resource Contention and Resource Access Control (RAC), Non-preemptive Critical Sections, Basic Priority-Inheritance and Priority-Ceiling Protocols, Stack Based Priority- Ceiling Protocol, Use of Priority-Ceiling Protocol in Dynamic Priority Systems, Preemption Ceiling Protocol, Access Control in Multiple-Module Resources, Controlling Concurrent Accesses to Data Objects. | 8 |
| IV | Real Time Communication Basic Concepts in Real time Communication, Soft and Hard RT Communication systems, Model of Real Time Communication, Priority-Based Service and Weighted Round-Robin Service Disciplines for Switched Networks, Medium Access Control Protocols for Broadcast Networks, Internet and Resource Reservation Protocols. | |
| V | Real Time Operating Systems and Databases Features of RTOS, Time Services, UNIX as RTOS, POSIX Issues, Characteristic of Temporal data, Temporal Consistency, Con-currency Control, Overview of Commercial Real Time databases. | 8 |

Text Books:

1. Real Time Systems – Jane W. S. Liu, Pearson Education Publication.

Reference Books:

1. Real Time Systems – Mall Rajib, Pearson Education
2. Real-Time Systems: Scheduling, Analysis, and Verification – Albert M. K. Cheng, Wiley.

Course Outcomes: At the end of this course students will demonstrate the ability to:

1. Describe concepts of Real-Time systems and modeling.
2. Recognize the characteristics of a real-time system in context with real time scheduling.
3. Classify various resource sharing mechanisms and their related protocols.
4. Interpret the basics of real time communication by the knowledge of real time models and protocols.
5. Apply the basics of RTOS in interpretation of real time systems.

KOE-062 EMBEDDED SYSTEM

COURSE OBJECTIVE: After completion of the course student will be able to:

1. Attain the knowledge of embedded system and its development environment.
2. Gain the knowledge of RTOS based embedded system design and its applications.

COURSE OUTCOME: After completion of the course student will be able to:

CO1: Understand the basics of embedded system and its structural units.

CO3: Analyze the embedded system specification and develop software programs.

CO3: Evaluate the requirements of the programming embedded systems, related software architecture.

CO3: Understand the RTOS based embedded system design.

CO3: Understand all the applications of the embedded system and designing issues.

| KOE-062 EMBEDDED SYSTEM | | |
|-------------------------|--|----------|
| Unit | Topic | Lectures |
| 1 | Introduction to Embedded Systems: Introduction to Embedded Systems – The build process for embedded systems- Structural units in Embedded processor , selection of processor & memory devices- DMA – Memory management methods- Timer and Counting devices, Watchdog Timer, Real Time Clock, In circuit emulator, Target Hardware Debugging. | 8 |
| 2 | Embedded Networking: Embedded Networking: Introduction, I/O Device Ports & Buses– Serial Bus communication protocols – RS232 standard – RS422 – RS485 – CAN Bus -Serial Peripheral Interface (SPI) – Inter Integrated Circuits (I2C) –need for device drivers. | 8 |
| 3 | Embedded Firmware Development Environment: Embedded Product Development Life Cycle objectives, different phases of EDLC, Modelling of EDLC; issues in Hardware-software Co-design, Data Flow Graph, state machine model, Sequential Program Model, concurrent Model, object oriented Model. | 8 |
| 4 | RTOS Based Embedded System Design: Introduction to basic concepts of RTOS- Task, process & threads, interrupt routines in RTOS, Multiprocessing and Multitasking, Preemptive and non preemptive scheduling, Task communication shared memory, message passing-, Inter process Communication – synchronization between processes-semaphores, Mailbox, pipes, priority inversion, priority inheritance, comparison of Real time Operating systems: Vx Works, 4C/OS-II, RT Linux. | 8 |
| 5 | Embedded System Application Development: Design issues and techniques Case Study of Washing Machine- Automotive Application- Smart card System Application. | 8 |

Text Books:

1. Wayne Wolf, “Computers as Components: Principles of Embedded Computer System Design”, Elsevier, 2006.
2. Michael J. Pont, “Embedded C”, Pearson Education , 2007.
3. Steve Heath, “Embedded System Design”, Elsevier, 2005.
4. Muhammed Ali Mazidi, Janice Gillispie Mazidi and Rolin D. McKinlay, “The 8051
5. Microcontroller and Embedded Systems”, Pearson Education, Second edition, 2007.

KOE-063 INTRODUCTION TO MEMS

COURSE OBJECTIVE: After completion of the course student will be able to:

1. Understand the Basic concept of MEMS, Mechanics of Beam and Diaphragm Structures, Air Damping and Electrostatic Actuation.
2. Know the knowledge of Thermal Effects and the Applications of MEMS in RF.

COURSE OUTCOME: After completion of the course student will be able to:

- CO1: Understand the Basic concept of MEMS Fabrication Technologies, Piezoresistance Effect, Piezoelectricity, Piezoresistive Sensor.
- CO2: Explain Mechanics of Beam and Diaphragm Structures.
- CO3: Understand the Basic concept of Air Damping and Basic Equations for Slide-film Air Damping, Couette-flow Model, Stokes-flow Model.
- CO4: Know the concept of Electrostatic Actuation.
- CO5: Understand the applications of MEMS in RF

| KOE-063 INTRODUCTION TO MEMS | | |
|------------------------------|--|----------|
| Unit | Topic | Lectures |
| 1 | Introduction to MEMS: MEMS Fabrication Technologies, Materials and Substrates for MEMS, Processes for Micromachining, Characteristics, Sensors/Transducers, Piezoresistance Effect, Piezoelectricity, Piezoresistive Sensor. | 8 |
| 2 | Mechanics of Beam and Diaphragm Structures: Stress and Strain, Hooke's Law. Stress and Strain of Beam Structures: Stress, Strain in a Bent Beam, Bending Moment and the Moment of Inertia, Displacement of Beam Structures Under Weight, Bending of Cantilever Beam Under Weight. | 8 |
| 3 | Air Damping: Drag Effect of a Fluid: Viscosity of a Fluid, Viscous Flow of a Fluid, Drag Force Damping, The Effects of Air Damping on Micro-Dynamics. Squeeze-film Air Damping: Reynolds' Equations for Squeeze-film Air Damping, Damping of Perforated Thick Plates. Slide-film Air Damping: Basic Equations for Slide-film Air Damping, Couette-flow Model, Stokes-flow Model. | 8 |
| 4 | Electrostatic Actuation: Electrostatic Forces, Normal Force, Tangential Force, Fringe Effects, Electrostatic Driving of Mechanical Actuators: Parallel-plate Actuator, Capacitive sensors. Step and Alternative Voltage Driving: Step Voltage Driving, Negative Spring Effect and Vibration Frequency. | 8 |
| 5 | Thermal Effects: Temperature coefficient of resistance, Thermo-electricity, Thermocouples, Thermal and temperature sensors. Applications of MEMS in RF MEMS Resonator Design Considerations, One-Port Micromechanical Resonator Modeling Vertical Displacement Two-Port Microresonator Modeling, Micromechanical Resonator Limitations. | 8 |

Text & Reference Books:

1. G. K. Ananthasuresh, K. J. Vinoy, S. Gopalakrishnan, K. N. Bhat and V. K. Atre, "Micro and smart systems", Wiley India, 2010.
2. S.M. Sze, "Semiconductor Sensors", John Wiley & Sons Inc., Wiley Interscience Pub.
3. M.J. Usher, "Sensors and Transducers", McMillian Hampshire.
4. RS Muller, Howe, Senturia and Smith, "Micro sensors", IEEE Press.

KOE-064 OBJECT ORIENTED PROGRAMMING

COURSE OBJECTIVE: After completion of the course student will be able to:

1. Understand the Basic concept of Object Orientation, object identity and Encapsulation.
2. Know the knowledge of Basic Structural Modeling, Object Oriented Analysis and C++ Basics.

COURSE OUTCOME: After completion of the course student will be able to:

CO1: Understand the Basic concept of Object Orientation, object identity and Encapsulation.

CO2: Understand the Basic concept of Basic Structural Modeling.

CO3: Know the knowledge of Object oriented design, Object design.

CO4: Know the knowledge of C++ Basics.

CO5: Understand the Basics of object and class in C++.

| KOE-064 OBJECT ORIENTED PROGRAMMING | | |
|-------------------------------------|--|----------|
| Unit | Topic | Lectures |
| 1 | Introduction: The meaning of Object Orientation, object identity, Encapsulation, information hiding, polymorphism, generosity, importance of modelling, principles of modelling, object oriented modelling, Introduction to UML, conceptual model of the UML, Architecture. | 8 |
| 2 | Basic Structural Modeling: Classes, Relationships, common Mechanisms, and diagrams. Class & Object Diagrams: Terms, concepts, modelling techniques for Class & Object Diagrams. Collaboration Diagrams: Terms, Concepts, depicting a message, polymorphism in collaboration Diagrams, iterated messages, use of self in messages. Sequence Diagrams: Terms, concepts, depicting asynchronous messages with/without priority, call-back mechanism, broadcast messages. Basic Behavioural Modeling: Use cases, Use case Diagrams, Activity Diagrams, State Machine, Process and thread, Event and signals, Time diagram, interaction diagram, Package diagram. Architectural Modeling: Component, Deployment, Component diagrams and Deployment diagrams | 8 |
| 3 | Object Oriented Analysis: Object oriented design, Object design, Combining three models, Designing algorithms, design optimization, Implementation of control, Adjustment of inheritance, Object representation, Physical packaging, Documenting design considerations. Structured analysis and structured design (SA/SD), Jackson Structured Development (JSD). Mapping object oriented concepts using non-object oriented language, Translating classes into data structures, Passing arguments to methods, Implementing inheritance, associations encapsulation. Object oriented programming style: reusability, extensibility, robustness, programming in the large. Procedural v/s OOP, Object oriented language features. Abstraction and Encapsulation. | 8 |
| 4 | C++ Basics : Overview, Program structure, namespace, identifiers, variables, constants, enum, operators, typecasting, control structures C++ Functions : Simple functions, Call and Return by reference, Inline functions, Macro Vs. Inline functions, Overloading of functions, default arguments, friend functions, virtual functions | 8 |
| 5 | Objects and Classes : Basics of object and class in C++, Private and public members, static data and function members, constructors and their types, destructors, operator overloading, type conversion. Inheritance : Concept of Inheritance, types of inheritance: single, multiple, multilevel, hierarchical, hybrid, protected members, overriding, virtual base class Polymorphism : Pointers in C++, Pointers and Objects, this pointer, virtual and pure virtual functions, Implementing polymorphism | 8 |

Text Books:

1. James Rumbaugh et. al, “Object Oriented Modeling and Design”, PHI
2. Grady Booch, James Rumbaugh, Ivar Jacobson, “The Unified Modeling Language User Guide”, Pearson Education
3. Object Oriented Programming with C++, E Balagurusamy, TMH

Reference Books:

1. R. S. Salaria, Mastering Object Oriented Programming with C++, Khanna Publishing House
2. C++ Programming, Black Book, Steven Holzner, dreamtech
3. Object Oriented Programming in Turbo C++, Robert Lafore, Galgotia
4. Object Oriented Programming with ANSI and Turbo C++, Ashok Kamthane, Pearson
5. The Complete Reference C++, Herbert Schlitz, TMH
6. C++ and Object Oriented Programming Paradigm, PHI
7. C++ : How to Program, 9th Edition, Deitel and Deitel, PHI

KOE 065 NUMERICAL TECHNIQUES

COURSE OBJECTIVE: Students undergoing this course are expected to-

1. Understand about the basics of numerical techniques and its applications to Engineering Problems.

COURSE OUTCOME: After completion of the course student will be able to-

CO1: Understand about the basics of Ordinary Differential Equations, Separable equations, Equations made separable by change of variables.

CO2: Retrieve the information content of Power series method.

CO3: Apply problem specific Bessel's equation, Bessel Functions to engineering applications.

CO4: Understand about the basics of matrix, Eigen values and eigen vectors.

CO5: Analysis of Stage wise Processes by the Calculus of Finite Differences, Countercurrent Liquid- Liquid Extraction.

| KOE 065 NUMERICAL TECHNIQUES | | |
|------------------------------|---|----------|
| Unit | Topic | Lectures |
| 1 | Ordinary Differential Equations, Separable equations, Equations made separable by change of variables, Homogeneous Equations, Equations with first order and first degree with linear coefficients, Exact equations, Linear equation of first order, Bernoulli's equation, Other integrating factors, Integration of Exact equations, Equations of first order and higher degree, Clairaut's equation, Singular solutions, Equations with missing terms, General properties of Linear equations, Linear equations with constant coefficients, Determination of the complementary function, exponential functions, Determination of the particular integral, the Euler equation, Simultaneous Linear Differential equations. | 8 |
| 2 | Power series method, theory of the power series method, Legendre's equation, Legendre's Polynomials, Frobenius Method. | 8 |
| 3 | Bessel's equation, Bessel Functions $J_\nu(x)$, Bessel Functions $J_\nu(x)$ for any $\nu \geq 0$. Gamma Function, Solution $J_\nu(x)$ of the Bessel Equation, Backbones of Bessel's Theory, $J_\nu(x)$ with $\nu = \pm 1/2, \pm 3/2, \pm 5/2$. | 8 |
| 4 | Definition of matrix, Some special definitions and operations involving matrices, Determinants, Theorems on determinants, Inverse of a matrix, Orthogonal and unitary matrix. Orthogonal vectors, System of linear equations, Systems on n equations with n unknowns, Cramer's Rule, Eigen values and eigen vectors. | 8 |
| 5 | Analysis of Stage wise Processes by the Calculus of Finite Differences, Countercurrent Liquid- Liquid Extraction, Solution of Difference Equations, Stirred-Tank Reactor System, Distillation in a Plate Column, Unsteady-state Operation, Starting a Stirred-tank Reactor, Rate at which a Plate Absorber Approaches Steady State. | 8 |

Text & Reference books:

1. Mickley, Reid and Sherwood, "Applied Mathematics in Chemical Engineering", Tata McGraw Hill, New Delhi (1981).
2. E. Kreyszig, "Advanced Engineering Mathematics", 8th edition, John Wiley and Sons (1999).
3. M. R. Spiegel, "Advanced Mathematics for Engineers and Scientists", Schaum Outline Series, McGraw Hill, (1971).
4. Chandrika Prasad, Reena Garg, "Advanced Engineering Mathematics", Khanna Publishing house

KOE 066 GIS & REMOTE SENSING

COURSE OBJECTIVE: *Students undergoing this course are expected to-*

1. Understand about the principles of GIS, Remote Sensing, Spatial Systems, and its applications to Engineering Problems.

COURSE OUTCOME: *After completion of the course student will be able to-*

CO1: Understand about the principles of Remote Sensing and its advantages and limitations.

CO2: Retrieve the information content of remotely sensed data.

CO3: Apply problem specific remote sensing data for engineering applications.

CO4: Analyze spatial and attribute data for solving spatial problems.

CO5: Create GIS and cartographic outputs for presentation

| KOE-066 GIS & REMOTE SENSING | | |
|------------------------------|---|----------|
| Unit | Topic | Lectures |
| 1 | Basic component of remote sensing (RS), advantages and limitations of RS, possible use of RS techniques in assessment and monitoring of land and water resources; electromagnetic spectrum, energy interactions in the atmosphere and with the Earth's surface; major atmospheric windows; principal applications of different wavelength regions; typical spectral reflectance curve for vegetation, soil and water, spectral signatures. | 8 |
| 2 | Different types of sensors and platforms; contrast ratio and possible causes of low contrast; aerial photography; types of aerial photographs, scale of aerial photographs, planning aerial photography- end lap and side lap; stereoscopic vision, requirements of stereoscopic photographs; air-photo interpretation- interpretation elements; | 8 |
| 3 | Photogrammetry- measurements on a single vertical aerial photograph, measurements on a stereo-pair- vertical measurements by the parallax method; ground control for aerial photography; satellite remote sensing, multispectral scanner- whiskbroom and push-broom scanner; different types of resolutions; analysis of digital data- image restoration; image enhancement; information extraction, image classification, unsupervised classification, supervised classification, important consideration in the identification of training areas, vegetation indices. | 8 |
| 4 | Microwave remote sensing. GI Sand basic components, different sources of spatial data, basic spatial entities, major components of spatial data, Basic classes of map projections and their properties. . | 8 |
| 5 | Methods of data input into GIS, Data editing, spatial data models and structures, Attribute data management, integrating data (map overlay) in GIS, Application of remote sensing and GIS for the management of land and water resources. | 8 |

Text & Reference Books:

1. Reddy Anji, M. 2006. Textbook of Remote Sensing and Geographical Information Systems. BS Publications, Hyderabad.
2. Elangovan, K. 2006. GIS Fundamentals Applications and Implementations. New India Publication Agency, New Delhi.
3. George Joseph. 2005. Fundamentals of Remote Sensing. 2nd Edition. Universities Press (India) Private Limited, Hyderabad.
4. Jensen, J.R. 2013. Remote Sensing of the Environment: An Earth Resource Perspective. Pearson Education Limited, UK.
5. Lillesand, T., R.W. Kiefer and J. Chipman. 2015. Remote Sensing and Image Interpretation. 7th Edition, John Wiley and Sons Singapore Pvt. Ltd., Singapore.
6. Sabins, F.F. 2007. Remote Sensing: Principles and Interpretation. Third Edition, Waveland Press Inc., Illinois, USA.

KOE-067 UNDERSTANDING THE HUMAN BEING COMPREHENSIVELY – HUMAN ASPIRATIONS AND ITS FULFILLMENT

Course Objectives:

1. To help the students having the clarity about human aspirations, goal, activities and purpose of life.
2. To facilitate the competence to understand the harmony in nature/existence and participation of human being in the nature/existence.
3. To help the students to develop the understanding of human tradition and its various components.

Course Methodology:

1. The methodology of this course is exploration and thus universally adaptable. It involves a systematic and rational study of the human being vis-à-vis the rest of existence.
2. It is free from any dogma or set of do's and don'ts related to values.
3. It is a process of self-investigation and self-exploration, and not of giving sermons. Whatever is found as truth or reality is stated as a proposal and the students are facilitated and encouraged to verify it in their own right, based on their Natural Acceptance and subsequent Experiential Validation.
4. This process of self-exploration takes the form of a dialogue between the teacher and the students to begin with, and then to continue within the student leading to continuous self-evolution.
5. This self-exploration also enables them to critically evaluate their preconditioning and present beliefs.

| KOE-067 UNDERSTANDING THE HUMAN BEING COMPREHENSIVELY- HUMAN ASPIRATIONS AND ITS FULFILLMENT | | |
|---|--|----------|
| Unit | Topic | Lectures |
| 1 | Introduction: The basic human aspirations and their fulfillment through Right understanding and Resolution; All-encompassing Resolution for a Human Being, its details and solution of problems in the light of Resolution. | 8 |
| 2 | Understanding Human being and its expansion: The domain of right understanding starts from understanding the human being (the knower, the experience and the doer); and extends up to understanding nature/existence – its interconnectedness and co-existence; and finally understanding the role of human being in existence (human conduct). | 8 |
| 3 | Activities of the Self: Understanding the human being comprehensively is the first step and the core theme of this course; human being as co-existence of the self and the body; the activities and potentialities of the self; Reasons for harmony/contradiction in the self. | 8 |
| 4 | Understanding Co-existence with other orders: The need and the process of inner evolution (through self-exploration, selfawareness and self-evaluation)- particularly awakening to activities of the Self: Realization, Understanding and Contemplation in the Self (Realization of Co-Existence, Understanding of Harmony in Nature and Contemplation of Participation of Human in this harmony/ order leading to comprehensive knowledge about the existence). | 8 |
| 5 | Expansion of harmony from self to entire existence: Understanding different aspects of All-encompassing Resolution (understanding, wisdom, science etc.), Holistic way of living for Human Being with All-encompassing Resolution covering all four dimensions of human endeavour viz., realization, thought, behavior and work (participation in the larger order) leading to harmony at all levels from self to Nature and entire Existence. | 8 |

Reference Books:

1. A Foundation Course in Human Values and Profession Ethics (Text Book and Teachers' Manual), R. R. Gaur, R. Sangal, G. P. Bagaria (2010), Excel Books, New Delhi [ISBN 978-8-174-46781-2]
2. Avartansheel Arthshastra, A. Nagraj, Divya Path Sansthan, Amarkantak, India
3. Economy of Permanence – (a quest for social order based on non-violence), J. C. Kumarappa (2010), Sarva-Seva-Sangh-Prakashan, Varansi, India
4. Energy and Equity, Ivan Illich (1974), The Trinity Press, Worcester & Harper Collins, USA
5. Ishandi Nau Upnishad, Shankaracharya, Geeta press, Gorakhpur,
6. Manav Vyavahar Darshan, A. Nagraj, Divya Path Sansthan, Amarkantak, India
7. Manaviya Sanvidhan, A. Nagraj, Divya Path Sansthan, Amarkantak, India