

INSTITUTE OF ENGINEERING AND TECHNOLOGY, LUCKNOW
ELECTRONICS AND COMMUNICATION ENGINEERING
Vth Semester Time Table (2024-2025)

3rd Year (V th Semester)					Room No: EC- 209		
DAY/TIME	9:10-10:00 AM	10:00-10:50 AM	10:50-11:40 AM	11:40-12:30 PM	2:00-2:50 PM	2:50-3:40 PM	3:40-4:30 PM
MONDAY		INC 501	IEC501AK	IEC 503RCS	IEC 551 EC2 AK//GF IEC 553 EC1 RCS/GF		
TUESDAY	IEC 054 GF		IEC 503 RCS	IEC 058 GF	IEC 552 EC1 GF/GF IEC 554 EC2 GF		
WEDNESDAY	IEC 503 RCS	IEC 502 GF		IEC 058 GF			
THURSDAY		IEC501 AK	IEC 058 GF	IEC 502 GF	IEC 552 EC2 GF/GF IEC 554 EC1 GF		
FRIDAY	IEC 551 EC1 AK IEC 553 EC2 RCS/GF		IEC 054 GF	IEC 502 GF	IEC 503 (T) EC2 RCS / IEC501 (T)EC1 AK	IEC501 AK	
SATURDAY			IEC 503(T)EC1RCS IEC501(T) EC2 AK	IEC 502 GF			

IEC 501	Integrated Circuit	AK	Er. Amit Kumar
IEC 502	Microprocessor & Microcontroller	GF	GF
IEC 503	Principles of Communication	RCS	Dr. R.C.S. Chauhan
Department Elective – I			
IEC 054	Advance Digital design using Verilog	GF	GF
Department Elective – II			
IEC 058	Optical Communication	GF	GF
Department Labs			
IEC 551	Integrated Circuit Lab (Room No-EC-211)	AK	Er. Amit Kumar,GF
IEC 552	Microprocessor & Microcontroller Lab (Room No-EC-107)	GF	GF/GF
IEC 553	Communication Engineering Lab (Room No-EC-201)	RCS	Dr. R.C.S. Chauhan,GF
IEC 554	Mini Project/ Internship	GF/GF	GF/GF

(Prof. Subodh Wairya)
Head ECD